

IXTY2P50PA

–500 V, –2 A PolarP™ MOSFET

P-Channel Enhancement Mode

**Features & Benefits:**

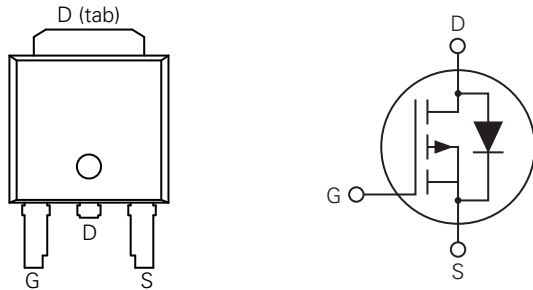
- AEC Q101 Qualified
- Avalanche Rated
- Rugged PolarP™ Process
- Low Package Inductance
- Easy to Mount
- Space Savings

Applications:

- High-side Switches
- Push Pull Amplifiers
- DC Choppers
- Automatic Test Equipment
- Current Regulators

Product Summary

Characteristics	Value	Unit
V_{DSS}	–500	V
$R_{DS(on),max}$	4.2	Ω
I_{D25}	–2	A

Pinout Diagram (TO-252)**G:** Gate; **D:** Drain; **S:** Source; **tab:** Drain

Maximum Ratings

Symbol	Characteristics	Conditions	Value	Units
V_{DSS}	Drain-Source Voltage	$T_J = 25\text{ °C to }150\text{ °C}$	-500	V
V_{GSS}	Gate-Source Voltage	Continuous	± 20	V
V_{GSM}		Transient	± 30	
I_{D25}	Drain Current	$T_C = 25\text{ °C}$	-2	A
I_{DM}		$T_C = 25\text{ °C}$, Pulse width limited by T_{JM}	-6	
I_A	Avalanche Current	$T_C = 25\text{ °C}$	-2	A
E_{AS}	Avalanche Energy	$T_C = 25\text{ °C}$	150	mJ
dV/dt	Reverse Diode dV/dt	$I_S \leq I_{DM}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150\text{ °C}$	10	V/ns
P_D	Power Dissipation	$T_C = 25\text{ °C}$	58	W
T_J	Operating Junction Temperature	-	-55 to +150	°C
T_{JM}	Maximum Junction Temperature	-	150	
T_{stg}	Storage Temperature	-	-55 to +150	
T_{sold}	Soldering Temperature	Plastic Body for 10 s	260	
W	Weight	-	0.35	g

Thermal Characteristics

Symbol	Characteristic	Value			Unit
		Min.	Typ.	Max.	
$R_{th, JC}$	Thermal Resistance, Junction-to-Case	-	-	2.15	°C/W

Electrical Characteristics – Static ($T_J = 25\text{ °C}$ unless otherwise specified)

Symbol	Characteristic	Conditions	Value			Unit
			Min.	Typ.	Max.	
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D = -250\text{ }\mu\text{A}$, $V_{GS} = 0\text{ V}$	-500	-	-	V
$V_{GS(th)}$	Gate Threshold Voltage	$I_D = -50\text{ }\mu\text{A}$, $V_{GS} = V_{DS}$	-2.5	-	-4.5	V
I_{GSS}	Gate-Source Leakage Current	$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 20\text{ V}$	-	-	± 50	nA
I_{DSS}	Drain-Source Current	$V_{DS} = V_{DSS}$, $V_{GS} = 0\text{ V}$	-	-	-1	μA
		$V_{DS} = V_{DSS}$, $V_{GS} = 0\text{ V}$, $T_J = 125\text{ °C}$	-	-	-5	
$R_{DS(on)}$	Drain-Source On-Resistance ¹	$V_{GS} = -10\text{ V}$, $I_D = 0.5 \times I_{D25}$	-	-	4.2	Ω

Note 1: Pulse test, $t \leq 300\text{ }\mu\text{s}$, duty cycle, $d \leq 2\%$

Electrical Characteristics – Dynamic ($T_J = 25\text{ }^\circ\text{C}$ unless otherwise specified)

Symbol	Characteristic	Conditions	Value			Unit
			Min.	Typ.	Max.	
g_{fs}	Transconductance ¹	$V_{DS} = -10\text{ V}, I_D = 0.5 \times I_{D25}$	1.4	2.4	–	S
C_{iss}	Input Capacitance	$V_{GS} = 0\text{ V}, V_{DS} = -25\text{ V}, f = 1\text{ MHz}$	–	600	–	pF
C_{oss}	Output Capacitance		–	70	–	
C_{rss}	Reverse Transfer Capacitance		–	12	–	
$Q_{g(on)}$	Total Gate Charge	$V_{GS} = -10\text{ V}, V_{DS} = 0.5 \times V_{DSS},$ $I_D = 0.5 \times I_{D25}$	–	11.9	–	nC
Q_{gs}	Gate-Source Charge		–	4.0	–	
Q_{gd}	Gate-Drain Charge		–	4.3	–	
$t_{d(on)}$	Turn-on Delay Time	Resistive Switching $V_{GS} = -10\text{ V}, V_{DS} = 0.5 \times V_{DSS},$ $I_D = 0.5 \times I_{D25}, R_{G(ext)} = 50\ \Omega$	–	26	–	ns
t_r	Rise Time		–	62	–	
$t_{d(off)}$	Turn-off Delay Time		–	54	–	
t_f	Fall Time		–	66	–	

Note 1: Pulse test, $t \leq 300\ \mu\text{s}$, duty cycle, $d \leq 2\%$

Source-Drain Diode Characteristics ($T_J = 25\text{ }^\circ\text{C}$ unless otherwise specified)

Symbol	Characteristic	Conditions	Value			Unit
			Min.	Typ.	Max.	
I_S	Continuous Diode Forward Current	$V_{GS} = 0\text{ V}$	–	–	-2	A
I_{SM}	Diode Pulse Current	Repetitive, Pulse width limited by T_{JM}	–	–	-8	A
V_{SD}	Diode Forward Voltage ¹	$I_F = -2\text{ A}, V_{GS} = 0\text{ V}$	–	–	-2.8	V
t_{rr}	Reverse Recovery Time	$I_F = -1\text{ A}, -di/dt = -100\text{ A}/\mu\text{s},$ $V_r = -100\text{ V}, V_{GS} = 0\text{ V}$	–	300	–	ns
Q_{rm}	Reverse Recovery Charge		–	3.9	–	μC
I_{rm}	Reverse Recovery Current		–	-26	–	A

Note 1: Pulse test, $t \leq 300\ \mu\text{s}$, duty cycle, $d \leq 2\%$

Characteristic Curves

Fig. 1. Output Characteristics @ $T_J = 25\text{ }^\circ\text{C}$

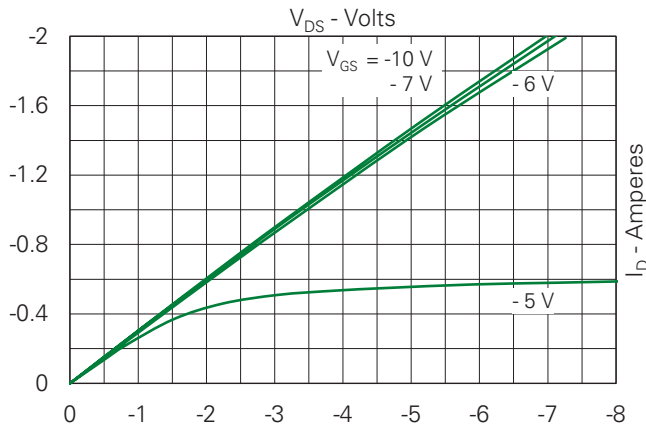


Fig. 2. Extended Output Characteristics @ $T_J = 25\text{ }^\circ\text{C}$

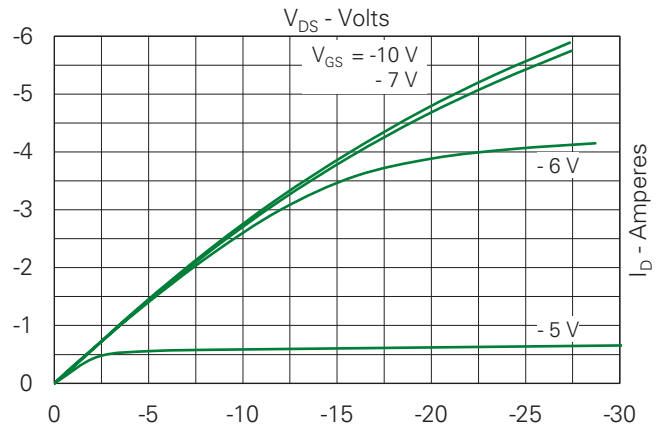


Fig. 3. Output Characteristics @ $T_J = 125\text{ }^\circ\text{C}$

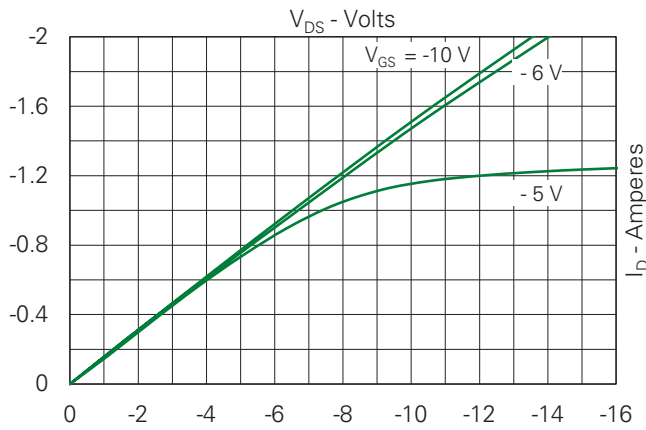


Fig. 4. $R_{DS(on)}$ Normalized to $I_D = -1\text{ A}$ Value vs. Junction Temperature

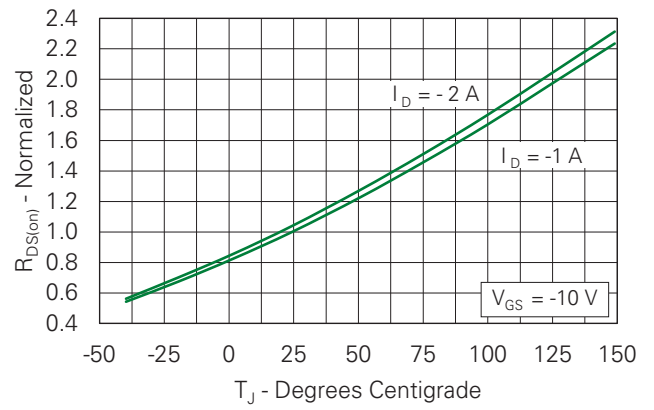


Fig. 5. $R_{DS(on)}$ Normalized to $I_D = -1\text{ A}$ Value vs. Drain Current

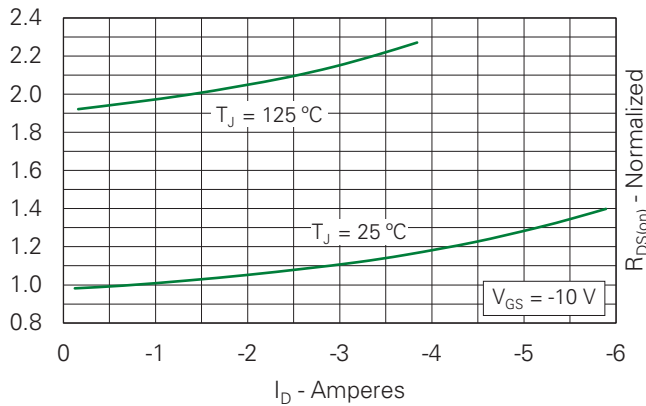


Fig. 6. Maximum Drain Current vs. Case Temperature

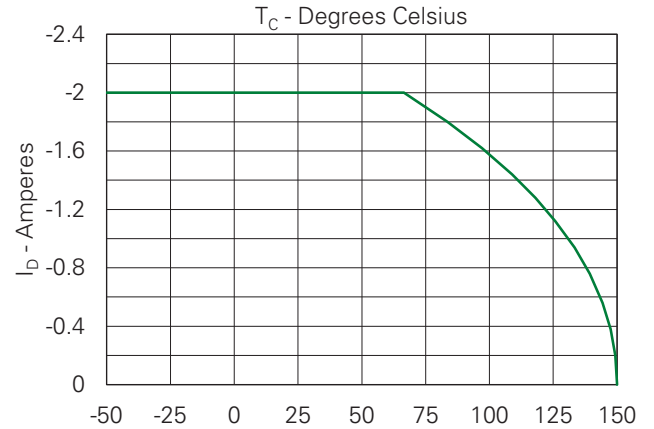


Fig. 7. Input Admittance

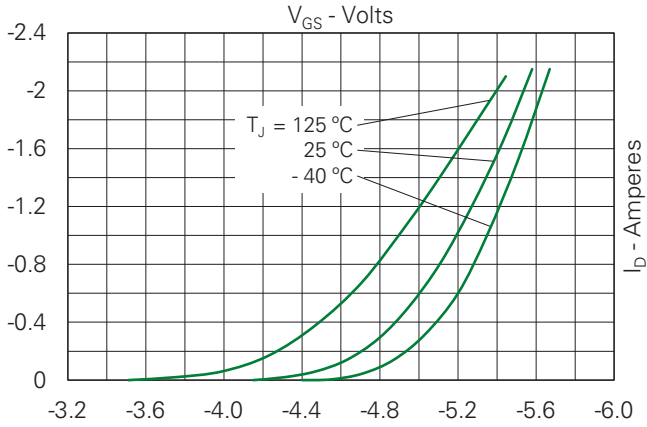


Fig. 8. Transconductance

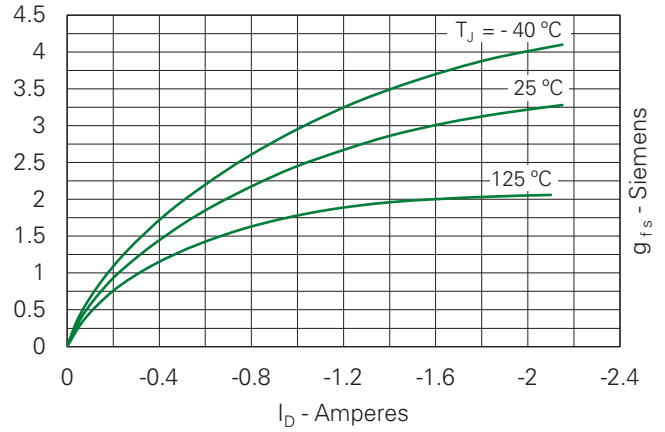


Fig. 9. Forward Voltage Drop of Intrinsic Diode

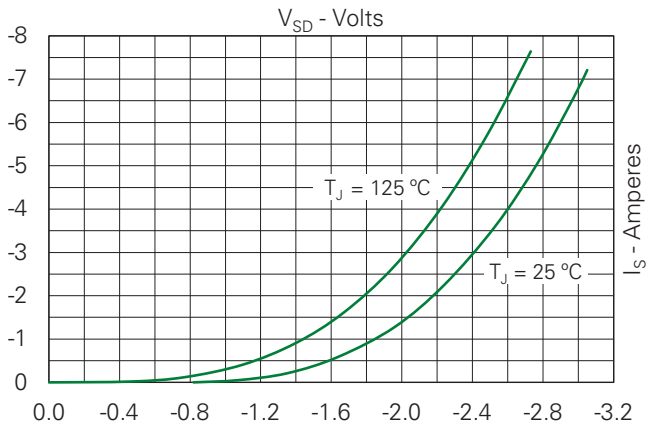


Fig. 10. Gate Charge

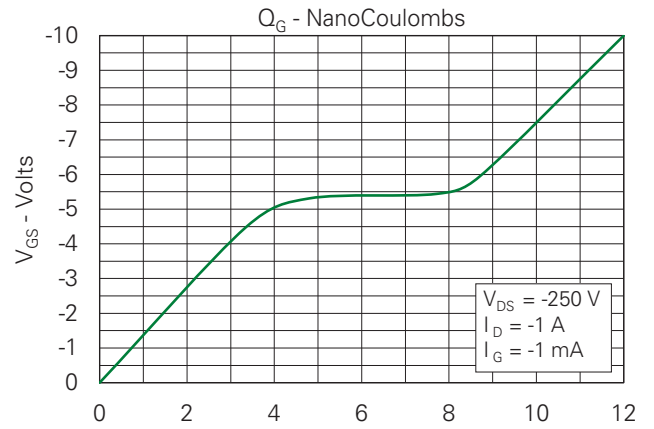


Fig. 11. Capacitance

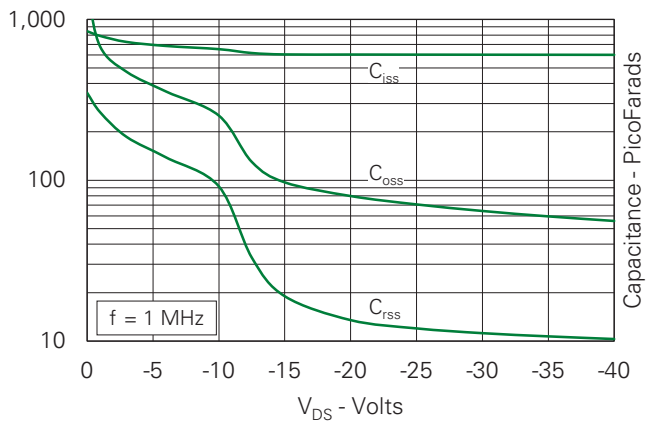


Fig. 12. Forward-Bias Safe Operating Area

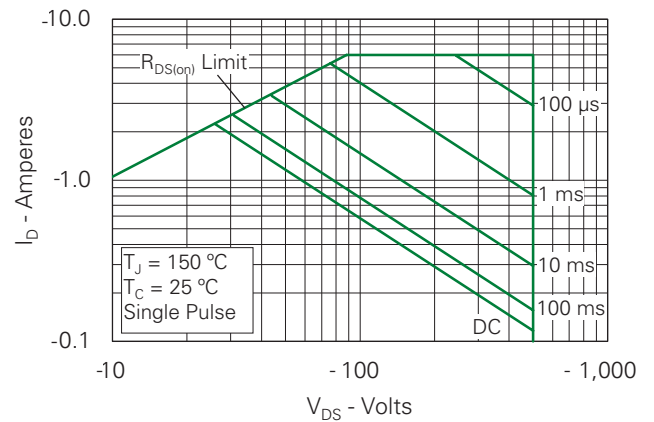
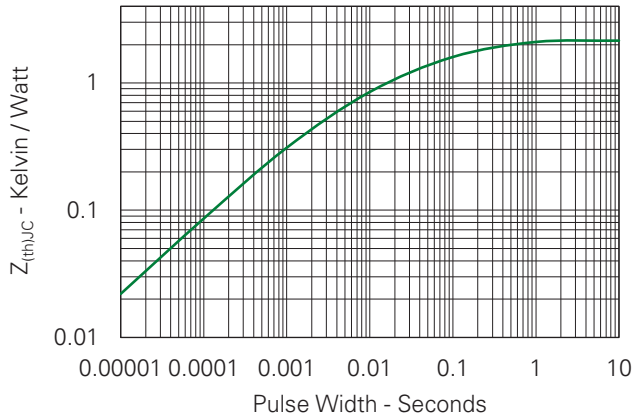
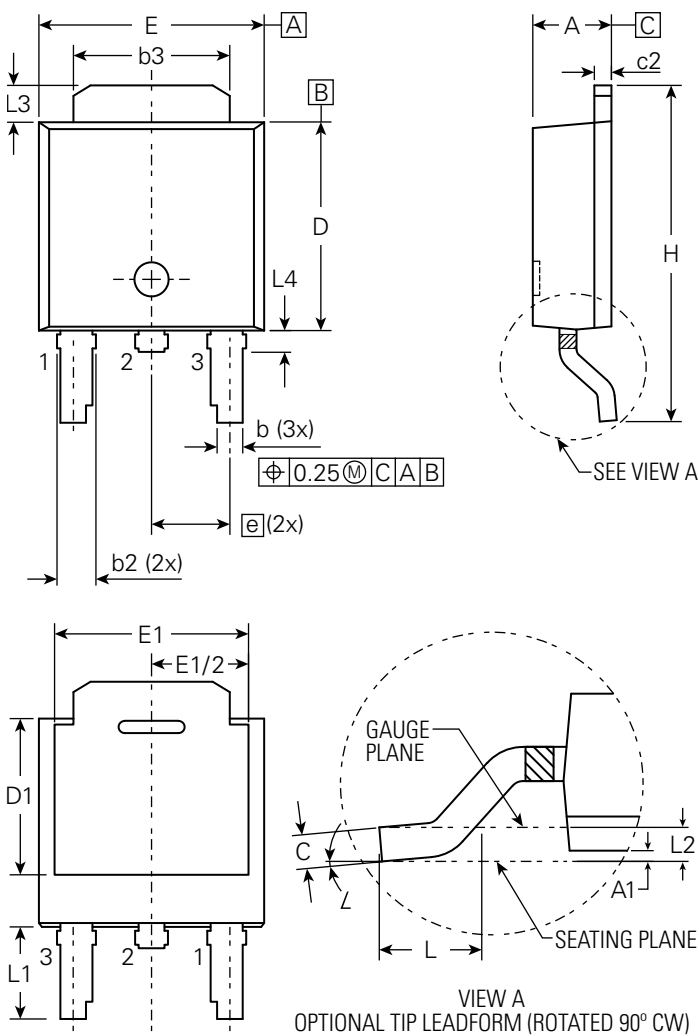


Fig. 13. Maximum Transient Thermal Impedance



Part Outline Drawing (TO-252)



VIEW A
OPTIONAL TIP LEADFORM (ROTATED 90° CW)

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