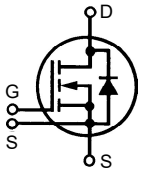
IXYS

Linear™ Power MOSFET w/Extended FBSOA

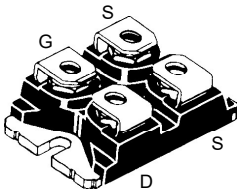
IXTN8N150L

V_{DSS} = 1500V
I_{D25} = 7.5A
$R_{DS(on)}$ ≤ 3.6Ω

N-Channel Enhancement Mode
Guaranteed FBSOA



miniBLOC, SOT-227 B
E153432



G = Gate D = Drain
S = Source S = Source

Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	T_J = 25°C to 150°C	1500	V
V_{DGR}	T_J = 25°C to 150°C, R_{GS} = 1MΩ	1500	V
V_{GSS}	Continuous	±30	V
V_{GSM}	Transient	±40	V
I_{D25}	T_C = 25°C	7.5	A
I_{DM}	T_C = 25°C, Pulse Width Limited by T_{JM}	20	A
P_D	T_C = 25°C	545	W
T_J		-55 to +150	°C
T_{JM}		150	°C
T_{stg}		-55 to +150	°C
V_{ISOL}	50/60 Hz, RMS, t = 1minute	2500	V~
	I_{ISOL} ≤ 1mA, t = 1s	3000	V~
M_d	Mounting Torque for Base Plate	1.5/13	Nm/lb.in.
	Terminal Connection Torque	1.3/11.5	Nm/lb.in.
Weight		30	g

Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
BV_{DSS}	V_{GS} = 0V, I_D = 1mA	1500			V
$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	5.0		8.0	V
I_{GSS}	V_{GS} = ±30V, V_{DS} = 0V			±200	nA
I_{DSS}	V_{DS} = V_{DSS}, V_{GS} = 0V			25	μA
	T_J = 125°C			500	μA
$R_{DS(on)}$	V_{GS} = 20V, I_D = 4A, Note 1			3.6	Ω

Features

- Designed for Linear Operations
- International Standard Package
- Molding Epoxies Meet UL94 V-0 Flammability Classification
- Guaranteed FBSOA at 60ºC
- miniBLOC with Aluminum Nitride Isolation
- Low $R_{DS(on)}$ HDMOS™ Process
- Rugged Polysilicon Gate Cell Structure
- Low Package Inductance

Advantages

- Easy to Mount
- Space Savings
- High Power Density

Applications

- Programmable Loads
- Current Regulators
- DC-DC Convertors
- Battery Chargers
- DC Choppers
- Temperature and Lighting Controls

Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
g_{fs}	V_{DS}= 50V, I_D = 4A, Note 1	1.4	2.3	3.2	S
C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		8000		pF
C_{oss}			405		pF
C_{rss}			70		pF
$t_{d(on)}$	**Resistive Switching Times**		36		ns
t_r	V_{GS} = 15V, V_{DS} = 0.5 • V_{DSS}, I_D = 4A		18		ns
$t_{d(off)}$	R_G = 2Ω (External)		90		ns
t_f			95		ns
$Q_{g(on)}$	V_{GS}= 15V, V_{DS} = 0.5 • V_{DSS}, I_D = 4A		250		nC
Q_{gs}			80		nC
Q_{gd}			116		nC
R_{thJC}				0.23	°C/W
R_{thCS}			0.05		°C/W

Safe Operating Area Specification

Symbol	Test Conditions	Characteristic Values Min.	Typ.	Max.	
SOA	V_{DS} = 1500V, I_D = 0.17A, T_C = 60°C, T_P = 3s	255			W

Source-Drain Diode

Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
I_S	V_{GS} = 0V			8	A
I_{SM}	Repetitive, Pulse Width Limited by T_{JM}			32	A
V_{SD}	I_F = 8A, V_{GS} = 0V, Note 1			1.2	V
t_{rr}	I_F = I_S, -di/dt = 100A/μs, V_R = 100V		1700		ns

Note: 1. Pulse Test, t ≤ 300μs; Duty Cycle, d ≤ 2%.

Fig. 1. Extended Output Characteristics @ T_J = 25ºC

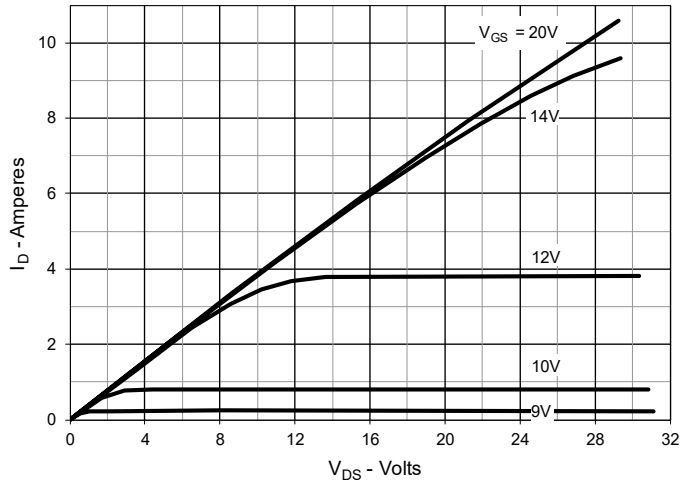


Fig. 2. Output Characteristics @ T_J = 125ºC

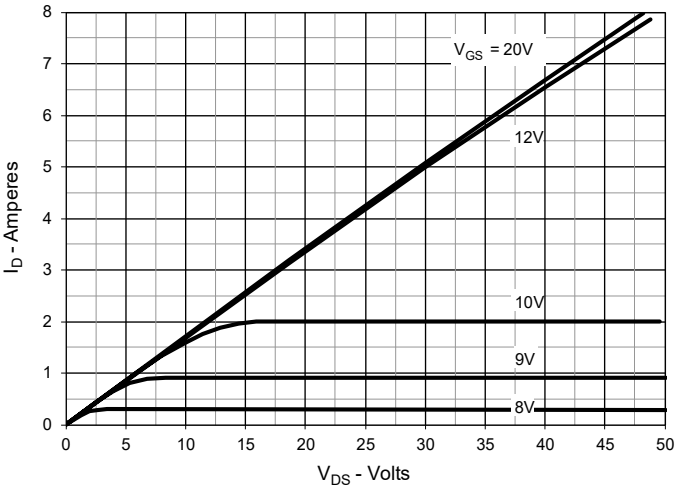


Fig. 3. $R_{DS(on)}$ Normalized to I_D = 4A Value vs. Junction Temperature

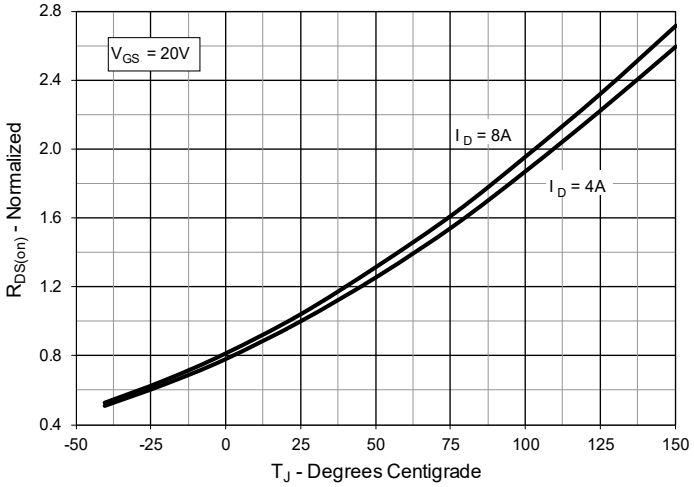


Fig. 4. $R_{DS(on)}$ Normalized to I_D = 4A Value vs. Drain Current

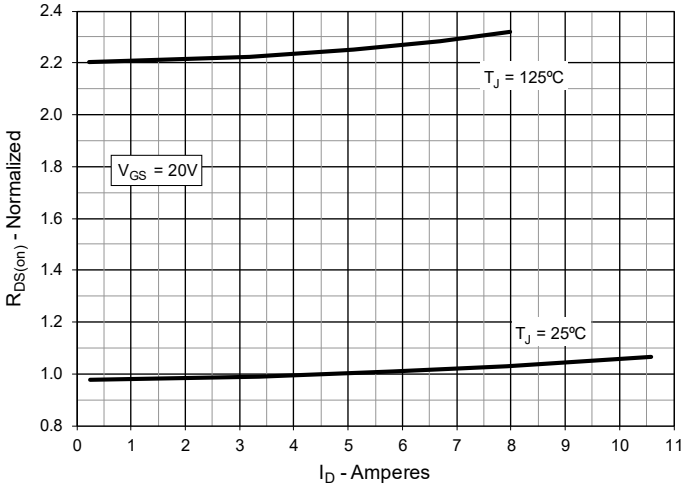


Fig. 5. Maximum Drain Current vs. Case Temperature

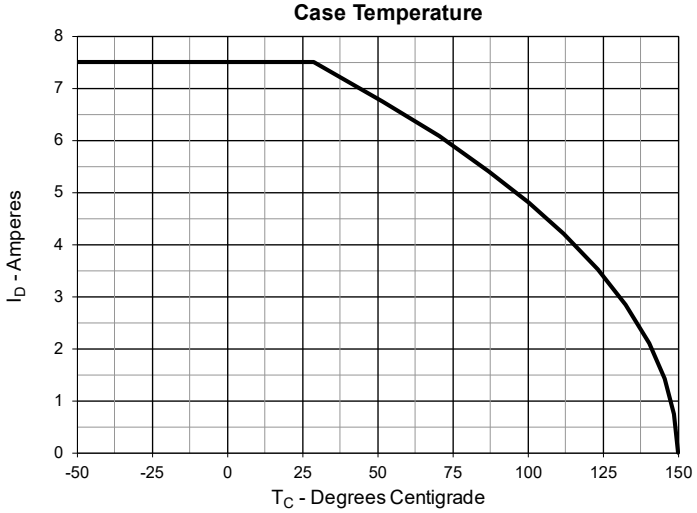


Fig. 6. Input Admittance

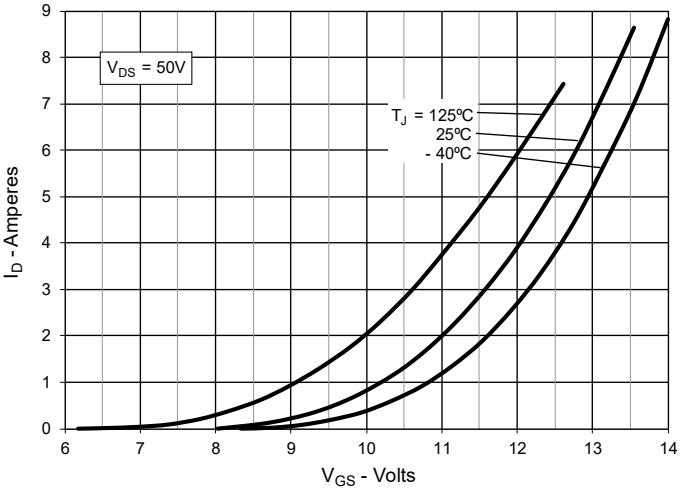


Fig. 7. Transconductance

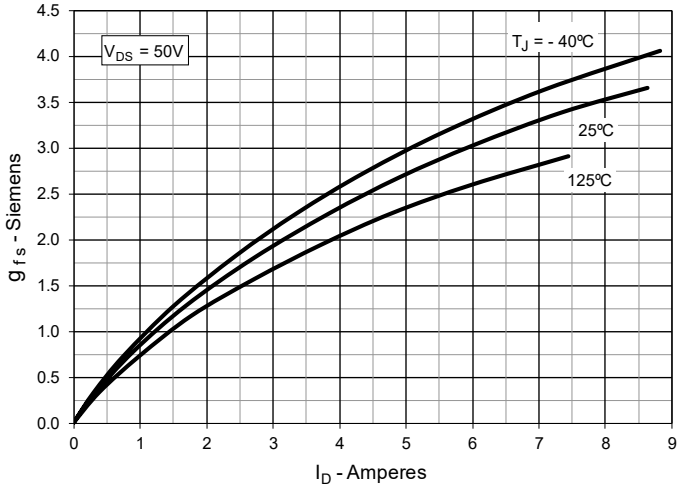


Fig. 8. Forward Voltage Drop of Intrinsic Diode

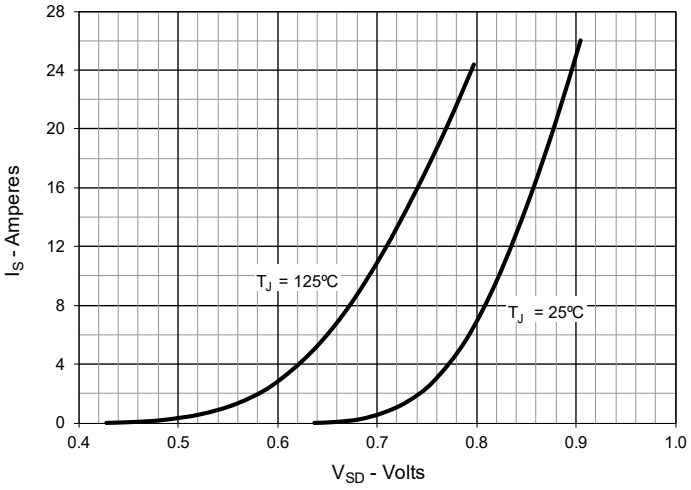


Fig. 9. Gate Charge

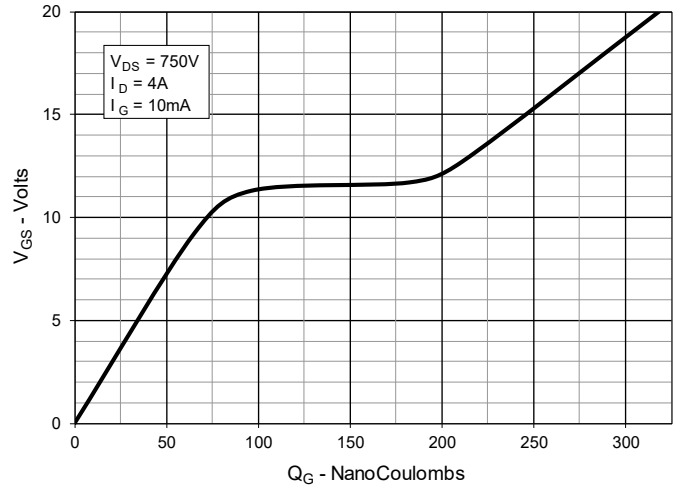


Fig. 10. Capacitance

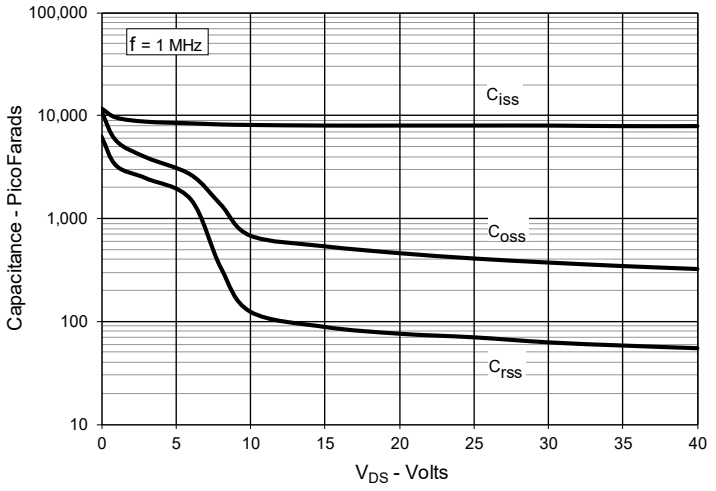
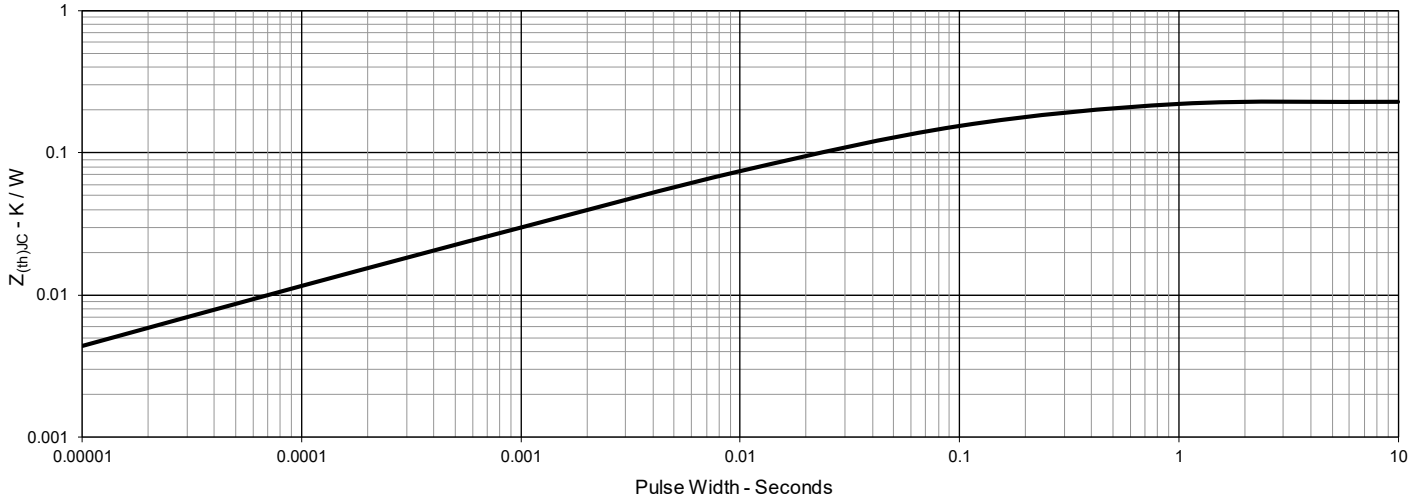


Fig. 11. Maximum Transient Thermal Impedance



Littelfuse reserves the right to change limits, test conditions, and dimensions.

Fig. 12. Forward-Bias Safe Operating Area @ T_C = 25ºC

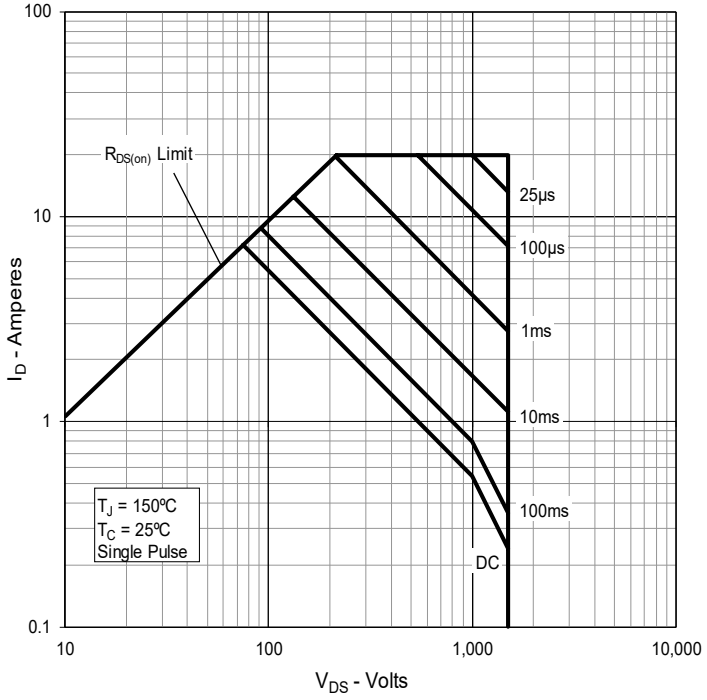
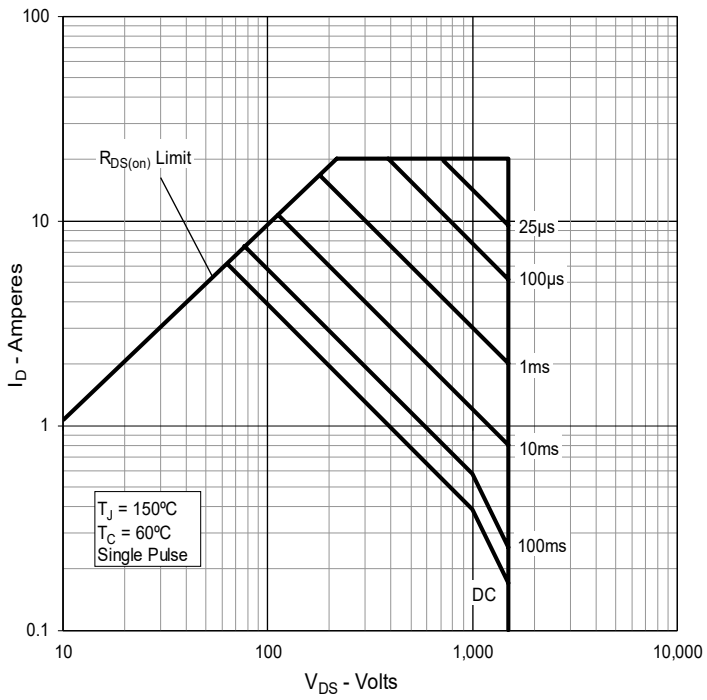
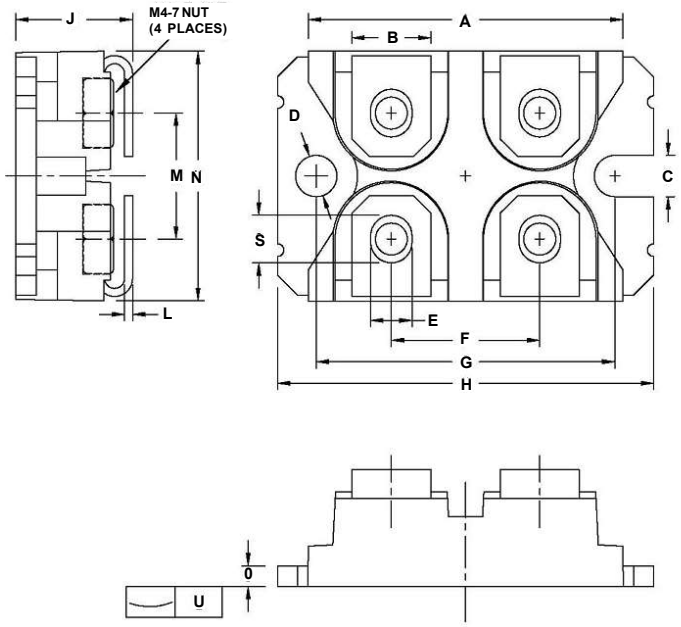


Fig. 13. Forward-Bias Safe Operating Area @ T_C = 60ºC



SOT-227 Outline



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	1.224	1.260	31.10	32.00
B	.303	.327	7.70	8.30
C	.161	.173	4.10	4.40
D	.161	.173	4.10	4.40
E	.161	.173	4.10	4.40
F	.587	.598	14.90	15.20
G	1.181	1.201	30.00	30.50
H	1.488	1.508	37.80	38.30
J	.461	.484	11.70	12.30
L	.030	.033	0.75	0.85
M	.492	.512	12.50	13.00
N	.984	1.004	25.00	25.50
O	.075	.087	1.90	2.20
S	.181	.193	4.60	4.90
U	.000	.005	0.00	0.13

1. NUT MATERIAL:
   STANDARD – Low carbon steel with Ni plating.
   OPTIONAL: – Brass Nut is available.
   PART NUMBER–BN
2. ALL METAL SURFACE ARE PRE NI PLATED EXCEPT TRIM AREA.

IXYS
A Littelfuse Technology