

# High Voltage XPT™ IGBT w/ Diode

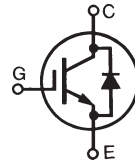
## IXYH10N170CV1

$$V_{CES} = 1700V$$

$$I_{C110} = 10A$$

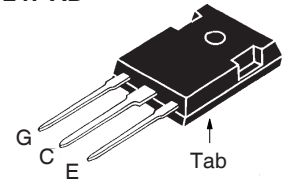
$$V_{CE(sat)} \leq 4.1V$$

$$t_{fi(typ)} = 70ns$$



Symbol	Test Conditions	Maximum Ratings	
$V_{CES}$	$T_J = 25^\circ C$ to $175^\circ C$	1700	V
$V_{CGR}$	$T_J = 25^\circ C$ to $175^\circ C$ , $R_{GE} = 1M\Omega$	1700	V
$V_{GES}$	Continuous	$\pm 20$	V
$V_{GEM}$	Transient	$\pm 30$	V
$I_{C25}$	$T_C = 25^\circ C$	36	A
$I_{C110}$	$T_C = 110^\circ C$	10	A
$I_{F110}$	$T_C = 110^\circ C$	22	A
$I_{CM}$	$T_C = 25^\circ C$ , 1ms	84	A
<b>SSOA (RBSOA)</b>	$V_{GE} = 15V$ , $T_{VJ} = 150^\circ C$ , $R_G = 10\Omega$	$I_{CM} = 40$	A
	Clamped Inductive Load	1360	V
$P_C$	$T_C = 25^\circ C$	280	W
$T_J$		-55 ... +175	$^\circ C$
$T_{JM}$		175	$^\circ C$
$T_{stg}$		-55 ... +175	$^\circ C$
$T_L$	Maximum Lead Temperature for Soldering	300	$^\circ C$
$T_{SOLD}$	1.6 mm (0.062in.) from Case for 10s	260	$^\circ C$
$M_d$	Mounting Torque	1.13/10	Nm/lb.in.
<b>Weight</b>		6	g

### TO-247 AD



G = Gate      C = Collector  
E = Emitter    Tab = Collector

### Features

- High Voltage Package
- High Blocking Voltage
- Low Saturation Voltage

### Advantages

- Low Gate Drive Requirement
- High Power Density

### Applications

- Switch-Mode and Resonant-Mode Power Supplies
- Uninterruptible Power Supplies (UPS)
- Laser Generators
- Capacitor Discharge Circuits
- AC Switches

Symbol	Test Conditions ( $T_J = 25^\circ C$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$BV_{CES}$	$I_C = 250\mu A$ , $V_{GE} = 0V$	1700		V
$V_{GE(th)}$	$I_C = 250\mu A$ , $V_{CE} = V_{GE}$	3.0		5.0 V
$I_{CES}$	$V_{CE} = V_{CES}$ , $V_{GE} = 0V$ $T_J = 150^\circ C$			25 $\mu A$ 5 mA
$I_{GES}$	$V_{CE} = 0V$ , $V_{GE} = \pm 20V$			$\pm 100$ nA
$V_{CE(sat)}$	$I_C = 10A$ , $V_{GE} = 15V$ , Note 1 $T_J = 150^\circ C$		3.6 4.9	4.1 V V

Symbol Test Conditions		Characteristic Values		
(T <sub>J</sub> = 25°C Unless Otherwise Specified)		Min.	Typ.	Max.
<b>g<sub>fs</sub></b>	I <sub>C</sub> = 10A, V <sub>CE</sub> = 10V, Note 1	5.4	9.0	S
<b>R<sub>Gi</sub></b>	Gate Input Resistance		10	Ω
<b>C<sub>ies</sub></b>	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz		930	pF
<b>C<sub>oes</sub></b>			74	pF
<b>C<sub>res</sub></b>			20	pF
<b>Q<sub>g(on)</sub></b>	I <sub>C</sub> = 10A, V <sub>GE</sub> = 15V, V <sub>CE</sub> = 0.5 • V <sub>CES</sub>		46	nC
<b>Q<sub>ge</sub></b>			6	nC
<b>Q<sub>gc</sub></b>			22	nC
<b>t<sub>d(on)</sub></b>	<b>Inductive load, T<sub>J</sub> = 25°C</b> I <sub>C</sub> = 10A, V <sub>GE</sub> = 15V V <sub>CE</sub> = 0.5 • V <sub>CES</sub> , R <sub>G</sub> = 10Ω Note 2		14	ns
<b>t<sub>ri</sub></b>			17	ns
<b>E<sub>on</sub></b>			1.4	mJ
<b>t<sub>d(off)</sub></b>			130	ns
<b>t<sub>fi</sub></b>			70	ns
<b>E<sub>off</sub></b>			0.7	mJ
<b>t<sub>d(on)</sub></b>	<b>Inductive load, T<sub>J</sub> = 150°C</b> I <sub>C</sub> = 10A, V <sub>GE</sub> = 15V V <sub>CE</sub> = 0.5 • V <sub>CES</sub> , R <sub>G</sub> = 10Ω Note 2		15	ns
<b>t<sub>ri</sub></b>			6	ns
<b>E<sub>on</sub></b>			2.3	mJ
<b>t<sub>d(off)</sub></b>			166	ns
<b>t<sub>fi</sub></b>			94	ns
<b>E<sub>off</sub></b>			0.9	mJ
<b>R<sub>thJC</sub></b>				0.53 °C/W
<b>R<sub>thCS</sub></b>		0.21		°C/W

### Reverse Diode (FRED)

Symbol Test Conditions		Characteristic Value		
(T <sub>J</sub> = 25°C, Unless Otherwise Specified)		Min.	Typ.	Max.
<b>V<sub>F</sub></b>	I <sub>F</sub> = 10A, V <sub>GE</sub> = 0V, Note 1 T <sub>J</sub> = 150°C		2.4	3.0 V
<b>I<sub>RM</sub></b>	I <sub>F</sub> = 10A, V <sub>GE</sub> = 0V, -di <sub>F</sub> /dt = 400A/μs, V <sub>R</sub> = 1200V, T <sub>J</sub> = 150°C		18	A
<b>t<sub>rr</sub></b>			160	ns
<b>R<sub>thJC</sub></b>				0.70 °C/W

### Notes:

1. Pulse test, t ≤ 300μs, duty cycle, d ≤ 2%.
2. Switching times & energy losses may increase for higher V<sub>CE</sub>(clamp), T<sub>J</sub> or R<sub>G</sub>.

### ADVANCE TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

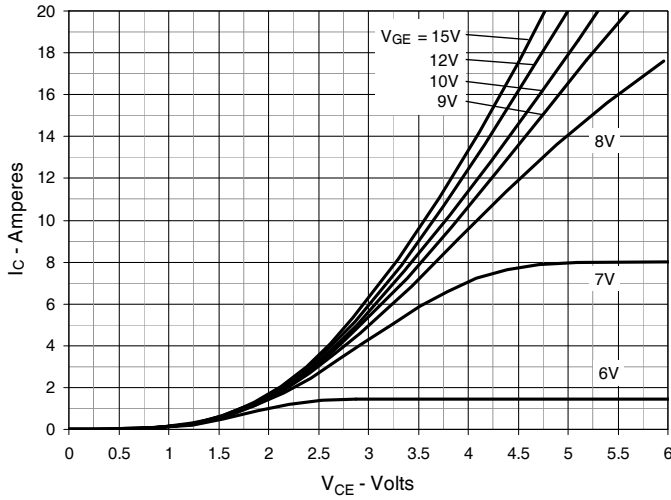
### TO-247 (IXYH) Outline

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.190	.205	4.83	5.21
A1	.090	.100	2.29	2.54
A2	.075	.085	1.91	2.16
b	.045	.055	1.14	1.40
b2	.075	.087	1.91	2.20
b4	.115	.126	2.92	3.20
C	.024	.031	0.61	0.80
D	.819	.840	20.80	21.34
D1	.650	.690	16.51	17.53
D2	.035	.050	0.89	1.27
E	.620	.635	15.75	16.13
E1	.545	.565	13.84	14.35
e	.215 BSC		5.45 BSC	
J	--	.010	--	0.25
K	--	.025	--	0.64
L	.780	.810	19.81	20.57
L1	.150	.170	3.81	4.32
øP	.140	.144	3.55	3.65
øP1	.275	.290	6.99	7.37
Q	.220	.244	5.59	6.20
R	.170	.190	4.32	4.83
S	.242 BSC		6.15 BSC	

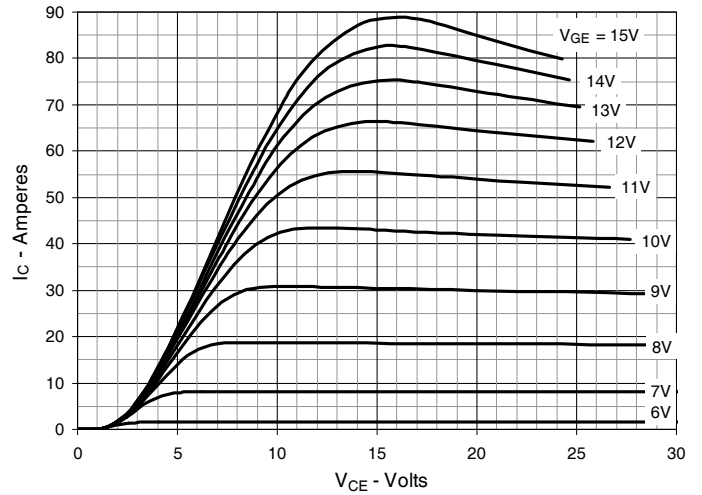
### IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:	4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065 B1	6,683,344	6,727,585	7,005,734 B2	7,157,338B2
	4,860,072	5,017,508	5,063,307	5,381,025	6,259,123 B1	6,534,343	6,710,405 B2	6,759,692	7,063,975 B2	
	4,881,106	5,034,796	5,187,117	5,486,715	6,306,728 B1	6,583,505	6,710,463	6,771,478 B2	7,071,537	

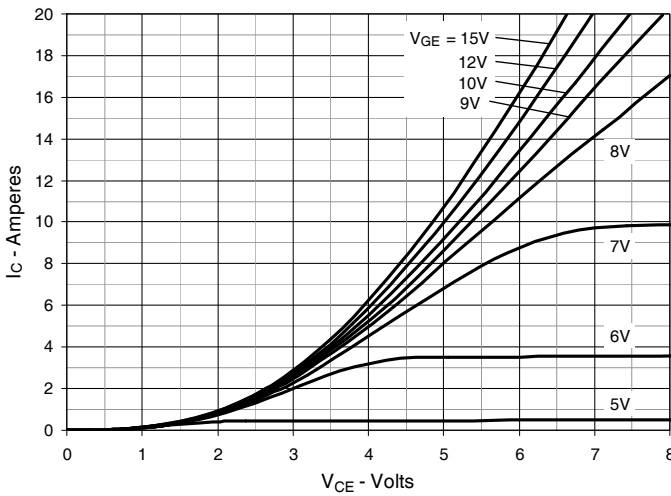
**Fig. 1. Output Characteristics @  $T_J = 25^\circ\text{C}$**



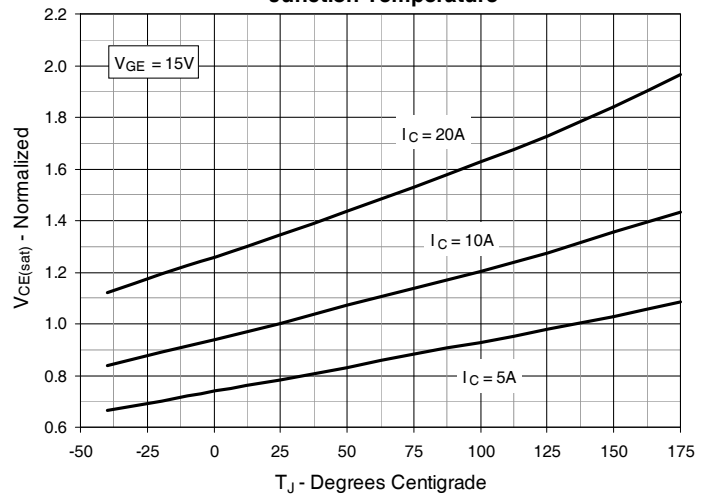
**Fig. 2. Extended Output Characteristics @  $T_J = 25^\circ\text{C}$**



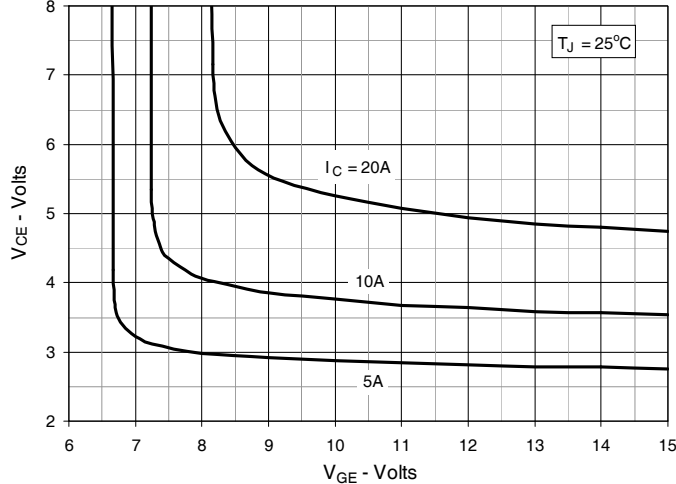
**Fig. 3. Output Characteristics @  $T_J = 150^\circ\text{C}$**



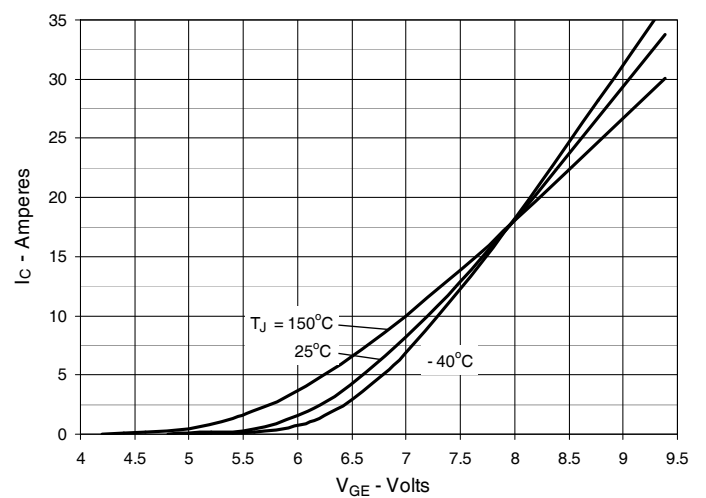
**Fig. 4. Dependence of  $V_{CE(sat)}$  on Junction Temperature**

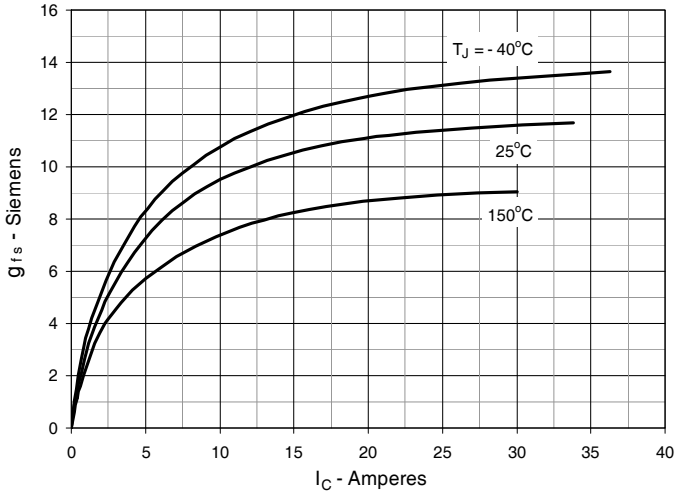
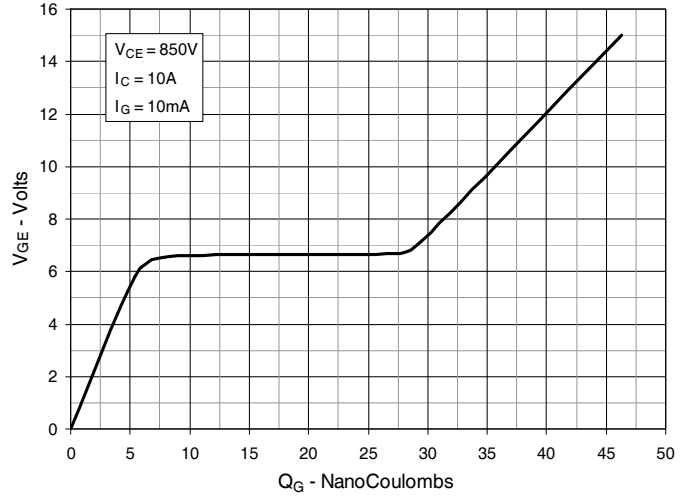
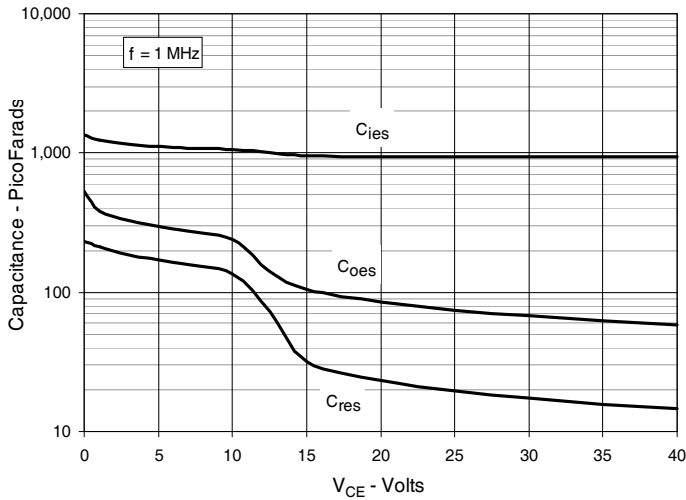
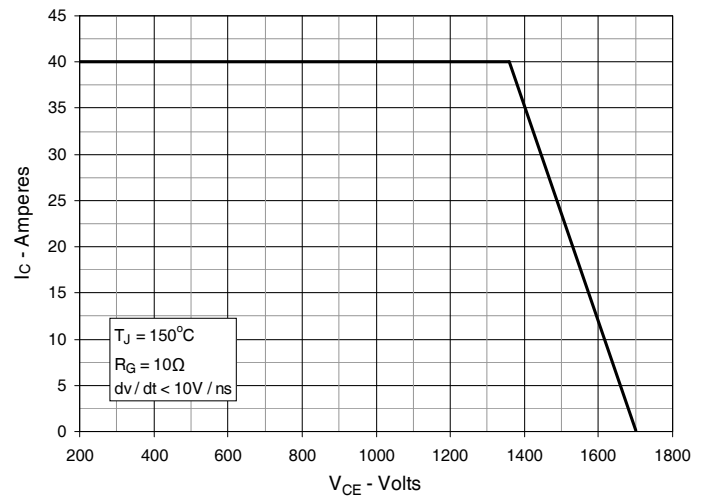
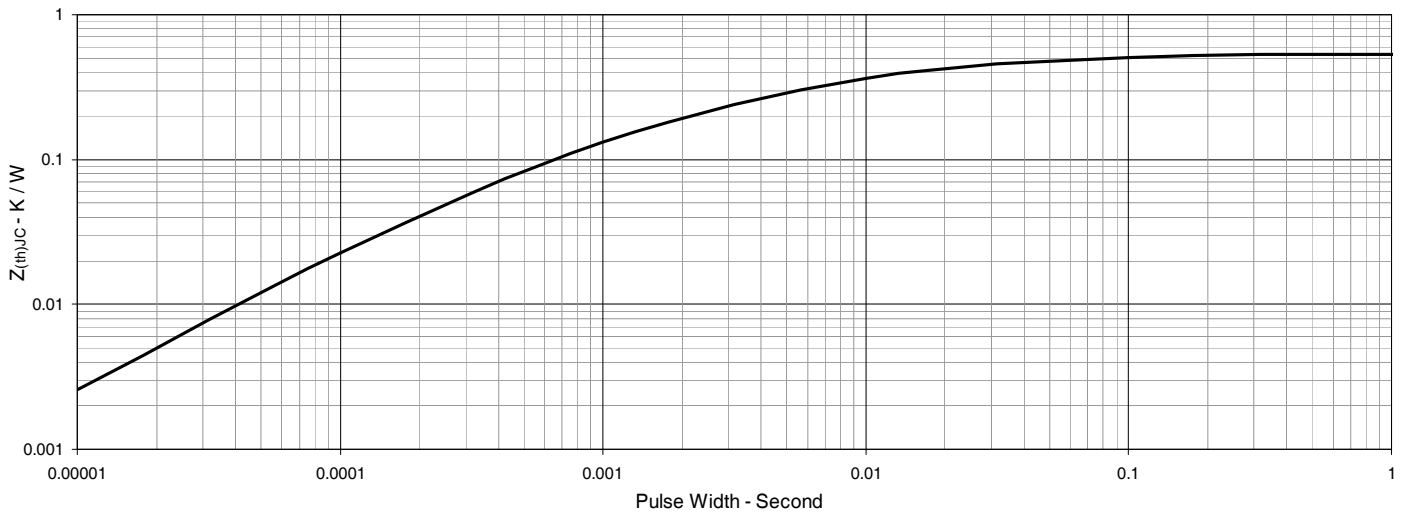


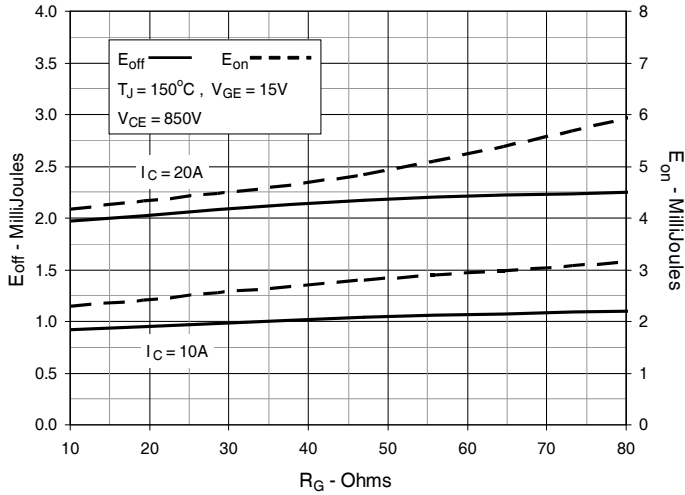
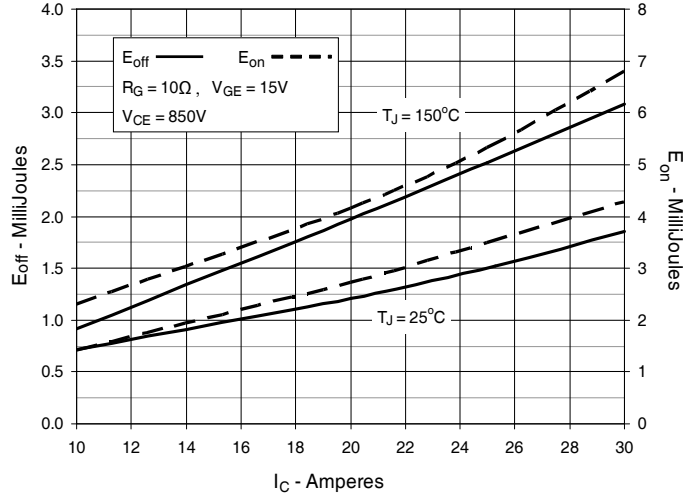
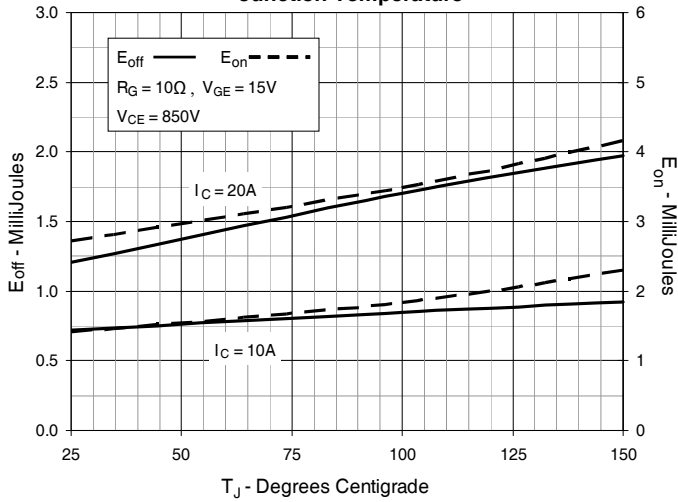
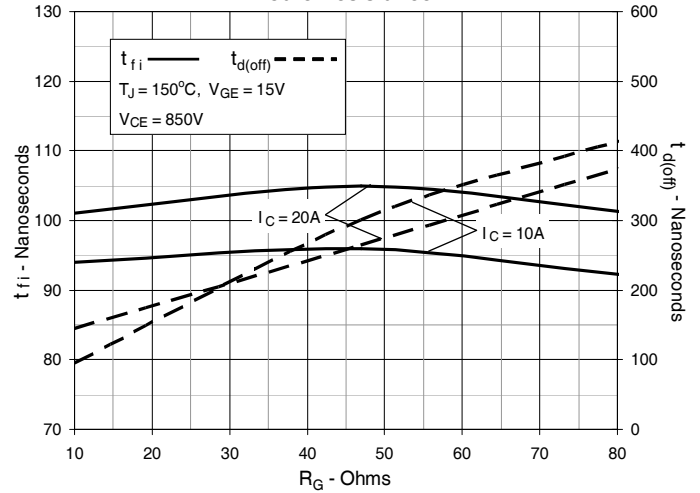
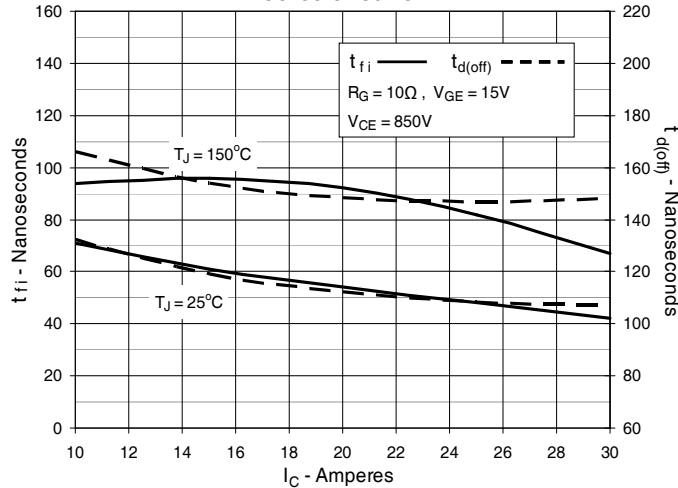
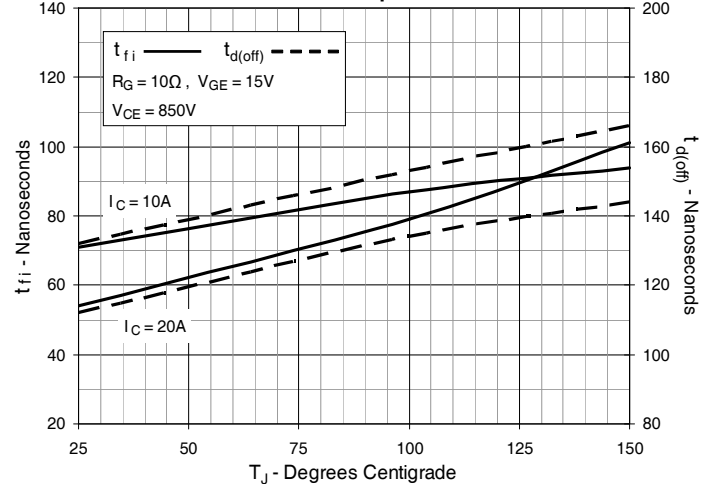
**Fig. 5. Collector-to-Emitter Voltage vs. Gate-to-Emitter Voltage**

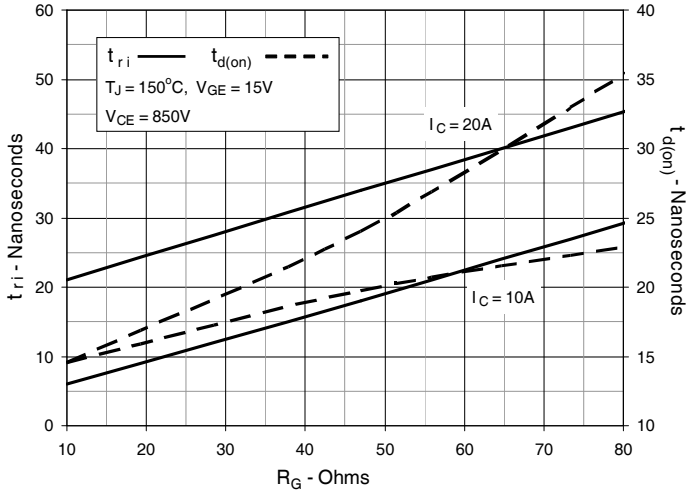
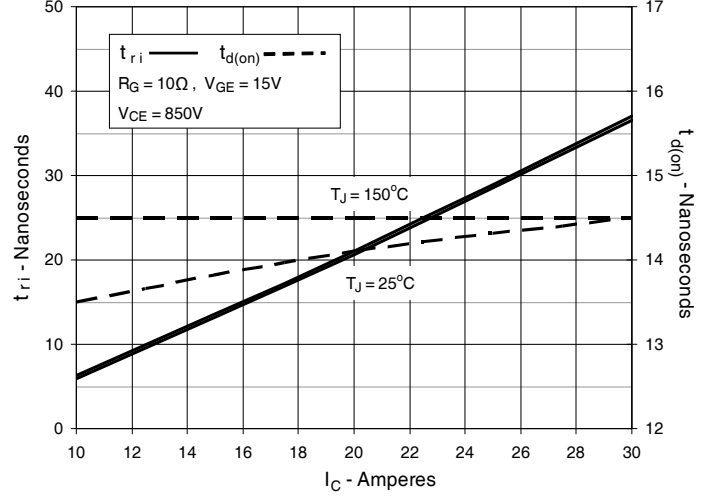
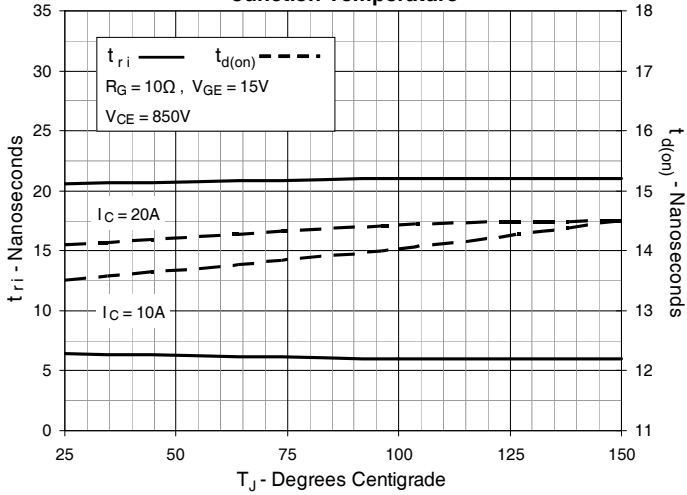


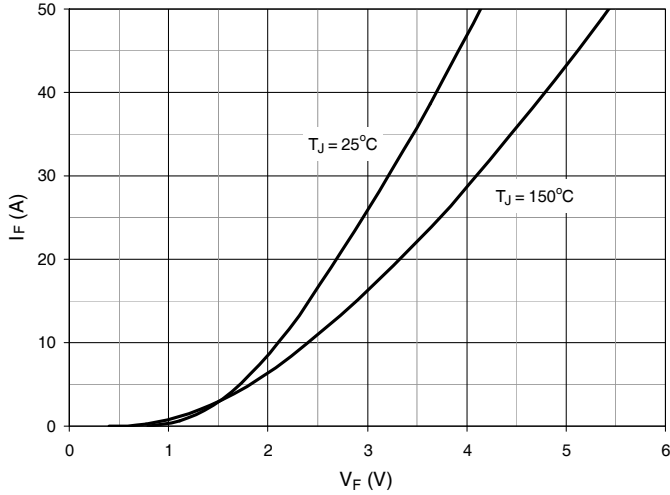
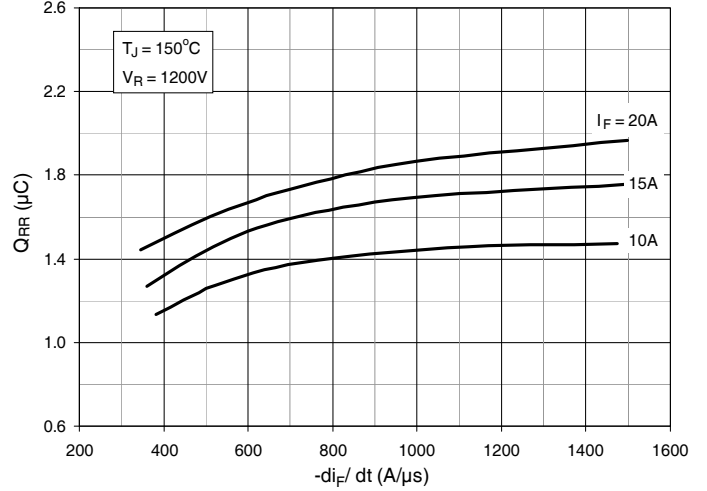
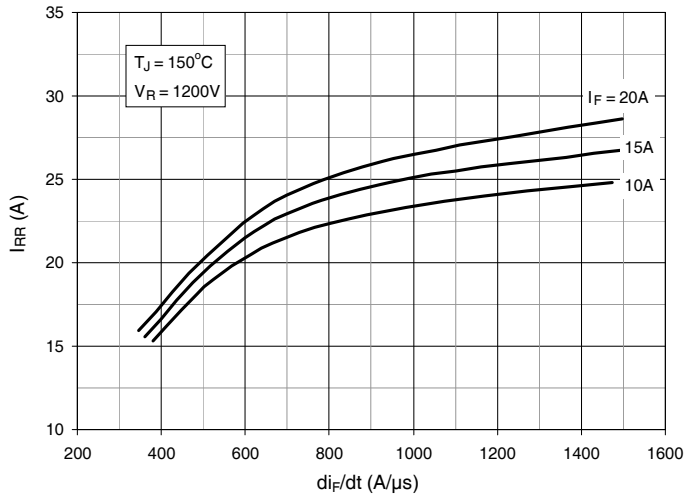
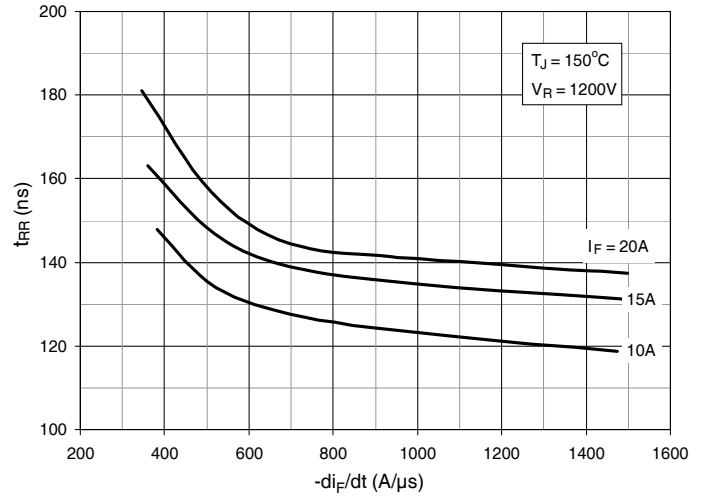
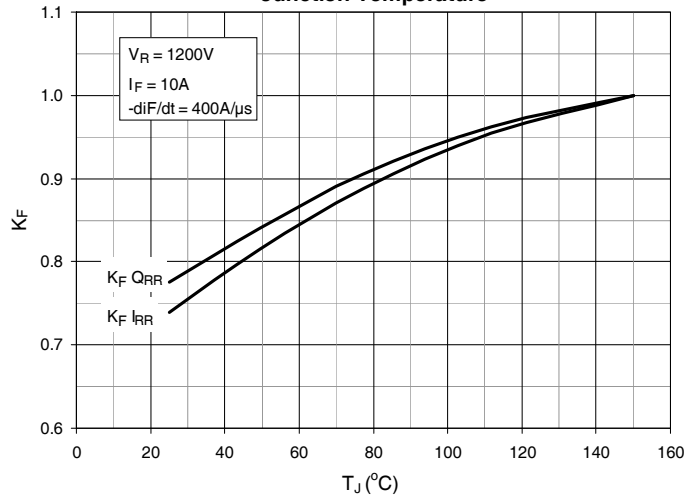
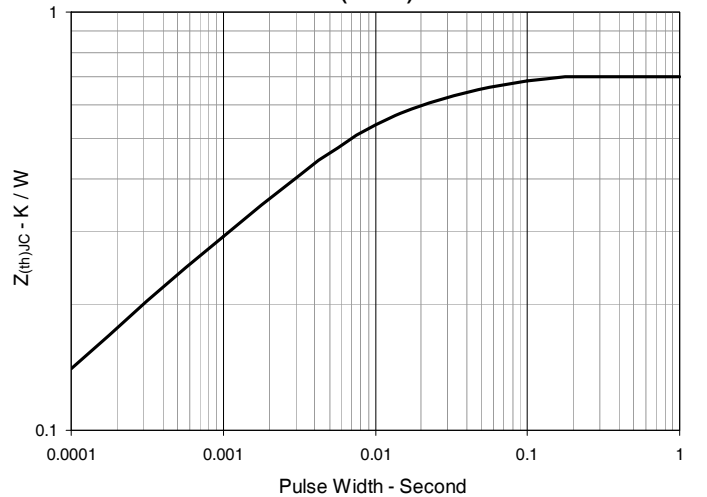
**Fig. 6. Input Admittance**



**Fig. 7. Transconductance**

**Fig. 8. Gate Charge**

**Fig. 9. Capacitance**

**Fig. 10. Reverse-Bias Safe Operating Area**

**Fig. 11. Maximum Transient Thermal Impedance (IGBT)**


**Fig. 12. Inductive Switching Energy Loss vs. Gate Resistance**

**Fig. 13. Inductive Switching Energy Loss vs. Collector Current**

**Fig. 14. Inductive Switching Energy Loss vs. Junction Temperature**

**Fig. 15. Inductive Turn-off Switching Times vs. Gate Resistance**

**Fig. 16. Inductive Turn-off Switching Times vs. Collector Current**

**Fig. 17. Inductive Turn-off Switching Times vs. Junction Temperature**


**Fig. 18. Inductive Turn-on Switching Times vs. Gate Resistance**

**Fig. 19. Inductive Turn-on Switching Times vs. Collector Current**

**Fig. 20. Inductive Turn-on Switching Times vs. Junction Temperature**


**Fig. 21. Diode Forward Characteristics**

**Fig. 22. Reverse Recovery Charge vs.  $-di_F/dt$** 

**Fig. 23. Reverse Recovery Current vs.  $-di_F/dt$** 

**Fig. 24. Reverse Recovery Time vs.  $-di_F/dt$** 

**Fig. 25. Dynamic Parameters  $Q_{RR}$ ,  $I_{RR}$  vs. Junction Temperature**

**Fig. 26. Maximum Transient Thermal Impedance (Diode)**




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