

| Parameter | Rating | Units |
|---------------------|--------|-----------------|
| Blocking Voltage | 60 | V _P |
| Load Current | 4 | A _{DC} |
| On-Resistance (max) | 0.09 | Ω |

Features

- Handle Load Currents Up to 4A_{DC}
- 2500V_{rms} Input/Output Isolation
- Power SIP Package
- High Reliability
- Low Drive Power Requirements
- No EMI/RFI Generation
- Flammability Rating UL 94 V-0

Applications

- Industrial Controls
- Motor Control
- Robotics
- Medical Equipment—Patient/Equipment Isolation
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- IC Equipment
- Home Appliances

Description

IXYS Integrated Circuits brings OptoMOS® technology, reliability, and compact size to a new family of high power solid state relays. The CPC1706Y, a DC-switching, normally open (1-Form-A) Solid State Relay, is part of that family.

Employing optically coupled MOSFET technology, the CPC1706Y provides 2500V_{rms} of input to output isolation. The relay output is constructed with efficient MOSFET switches that use IXYS Integrated Circuits' patented OptoMOS architecture. The input, a highly efficient infrared LED, controls the optically coupled output.

The combination of low on-resistance and high load current handling capability makes this relay suitable for a variety of high performance switching applications.

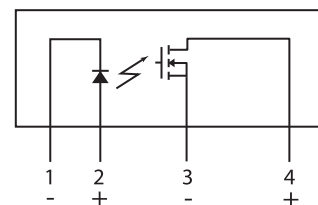
Approvals

- UL 508 Certified Component: File E69938

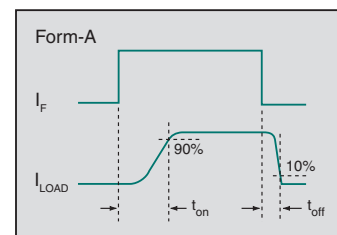
Ordering Information

| Part # | Description |
|----------|--|
| CPC1706Y | 4-Pin (8-Pin Body) Power SIP Package (25 per tube) |

Pin Configuration



Switching Characteristics of Normally Open Devices



Absolute Maximum Ratings @ 25°C

| Parameter | Symbol | Rating | Unit |
|--------------------------------------|-----------|-------------|-----------|
| Blocking Voltage | V_L | 60 | V_P |
| Reverse Input Voltage | V_R | 5 | V |
| Input control Current | I_F | 50 | mA |
| Peak (10ms) | | 1 | A |
| Input Power Dissipation ¹ | P_{IN} | 150 | mW |
| Total Power Dissipation ² | P_T | 2400 | mW |
| Isolation Voltage, Input to Output | V_{ISO} | 2500 | V_{rms} |
| Operational Temperature, Ambient | T_A | -40 to +85 | °C |
| Storage Temperature | T_{STG} | -40 to +125 | °C |

¹ Derate linearly 1.33 mW / °C

² Derate output power linearly 20 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

Electrical Characteristics @ 25°C (Unless Otherwise Noted)

| Parameter | Conditions | Symbol | Min | Typ | Max | Units |
|-------------------------------------|----------------------------|------------|-----|-------|------|----------|
| Output Characteristics | | | | | | |
| Blocking Voltage | $I_L=1\mu A$ | V_{DRM} | 60 | - | - | V |
| Load Current | Free Air, $I_F=5mA$ | | | | | |
| Continuous | - | I_L | - | - | 4 | A_{DC} |
| Peak | $t=10ms$ | I_{LPK} | - | - | 9 | A |
| On-Resistance ¹ | $I_F=5mA, I_L=1A$ | R_{ON} | - | 0.07 | 0.09 | Ω |
| Off-State Leakage Current | $I_F=0mA, V_L=60V_P$ | I_{LEAK} | - | - | 1 | μA |
| Switching Speeds | | | | | | |
| Turn-On | $I_F=5mA, V_L=10V$ | t_{on} | - | 0.5 | 5 | ms |
| Turn-Off | | t_{off} | - | 0.085 | 2 | |
| Output Capacitance | $I_F=0mA, V_L=50V, f=1MHz$ | C_{OUT} | - | 75 | - | pF |
| Input Characteristics | | | | | | |
| Input Control Current to Activate | $I_L=1A$ | I_F | - | 1.4 | 5 | mA |
| Input Control Current to Deactivate | - | I_F | 0.4 | - | - | mA |
| Input Voltage Drop | $I_F=5mA$ | V_F | 0.9 | 1.36 | 1.5 | V |
| Reverse Input Current | $V_R=5V$ | I_R | - | - | 10 | μA |
| Input/Output Characteristics | | | | | | |
| Capacitance Input-to-Output | $V_{IO}=0V, f=1MHz$ | C_{IO} | - | 2 | - | pF |

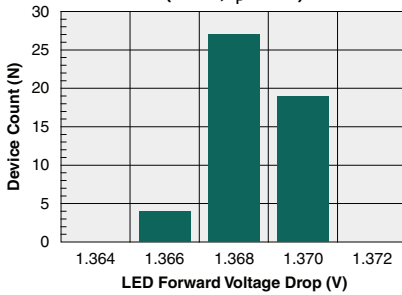
¹ Measurement taken within 1 second of on-time.

Thermal Characteristics

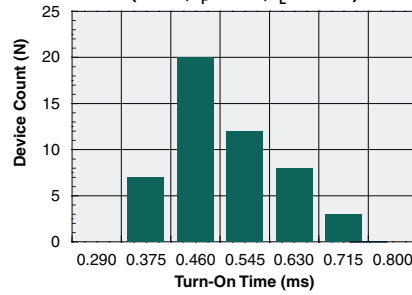
| Parameter | Conditions | Symbol | Rating | Units |
|--------------------------------------|------------|---------------|--------|-------|
| Thermal Impedance (junction to case) | - | θ_{JC} | 1.5 | °C/W |

PERFORMANCE DATA*

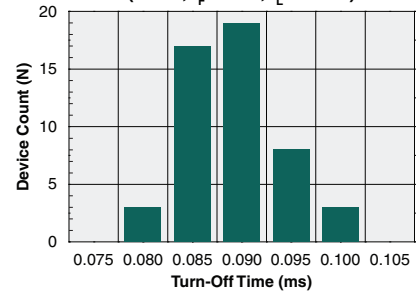
Typical LED Forward Voltage Drop
(N=50, $I_F=5mA$)



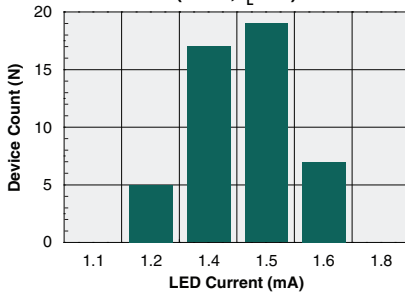
Typical Turn-On Time
(N=50, $I_F=5mA$, $I_L=75mA$)



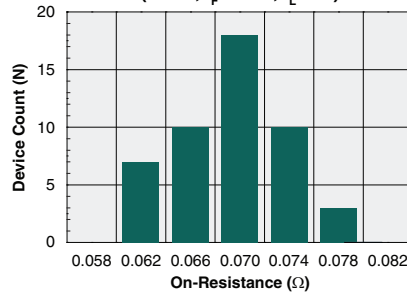
Typical Turn-Off Time
(N=50, $I_F=5mA$, $I_L=75mA$)



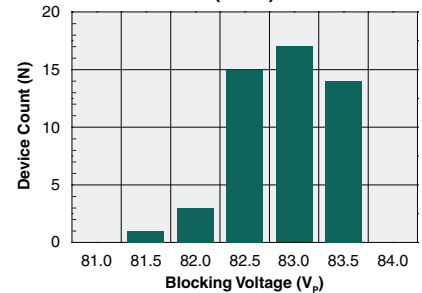
Typical I_F for Switch Operation
(N=50, $I_L=1A$)



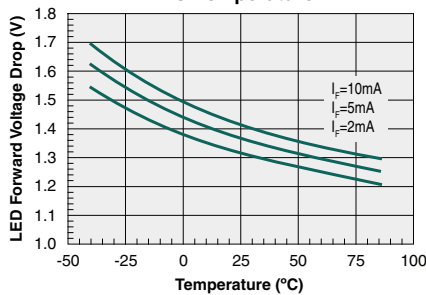
Typical On-Resistance Distribution
(N=50, $I_F=5mA$, $I_L=1A$)



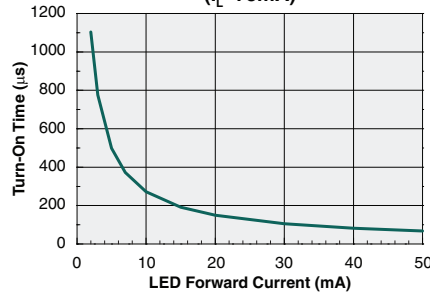
Typical Blocking Voltage Distribution
(N=50)



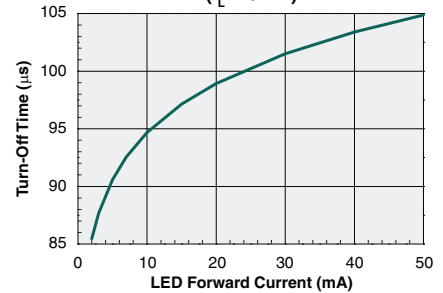
Typical LED Forward Voltage Drop
vs. Temperature



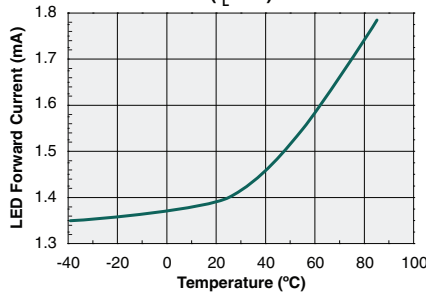
Typical Turn-On
vs. LED Forward Current
($I_L=75mA$)



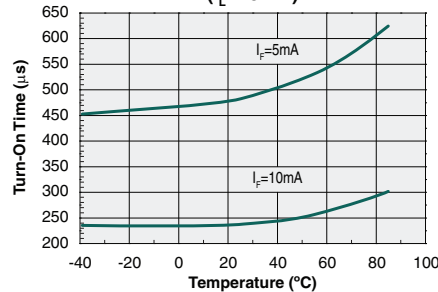
Typical Turn-Off
vs. LED Forward Current
($I_L=75mA$)



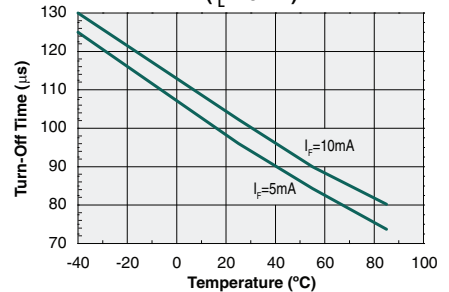
Typical I_F for Switch Operation
vs. Temperature
($I_L=1A$)



Typical Turn-On vs. Temperature
($I_L=75mA$)

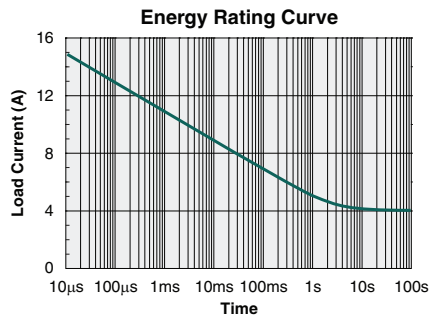
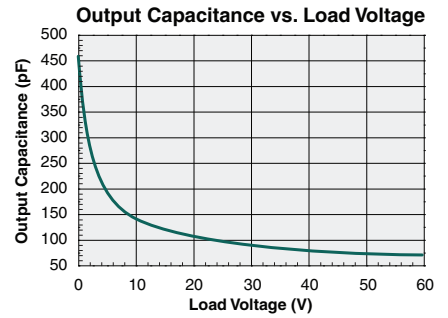
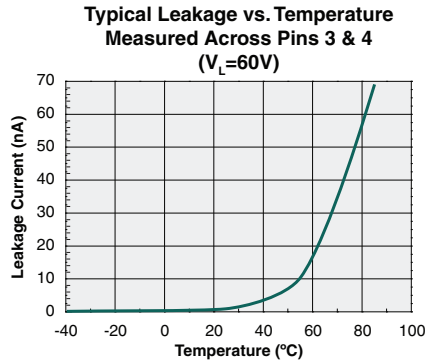
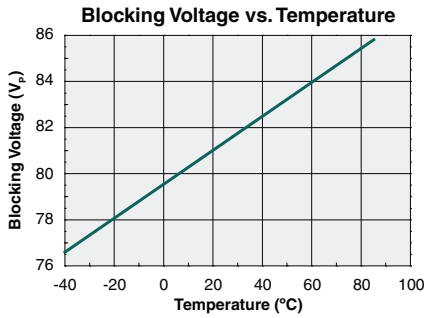
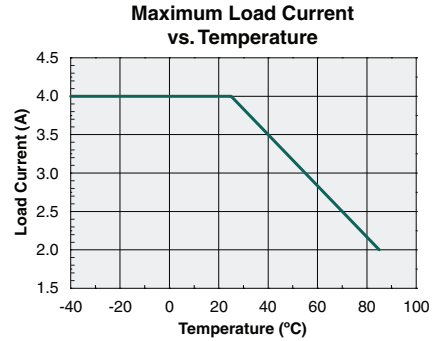
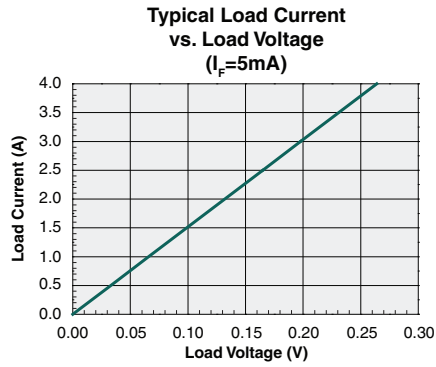
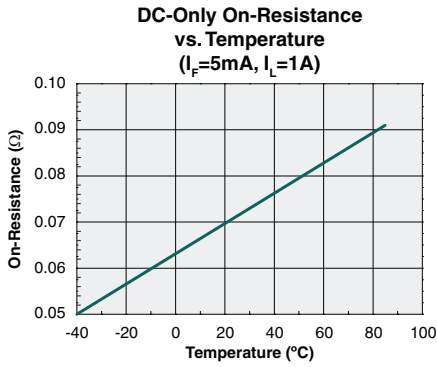


Typical Turn-Off vs. Temperature
($I_L=75mA$)



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

PERFORMANCE DATA*



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

Manufacturing Information

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

The Maximum Solder Temperature and the Maximum Total Dwell Time in all solder waves the device pins (leads) may be at the Maximum Solder Temperature is given in the table below. The body temperature of the device must not exceed the Maximum Body Temperature shown below at any time during the soldering process.

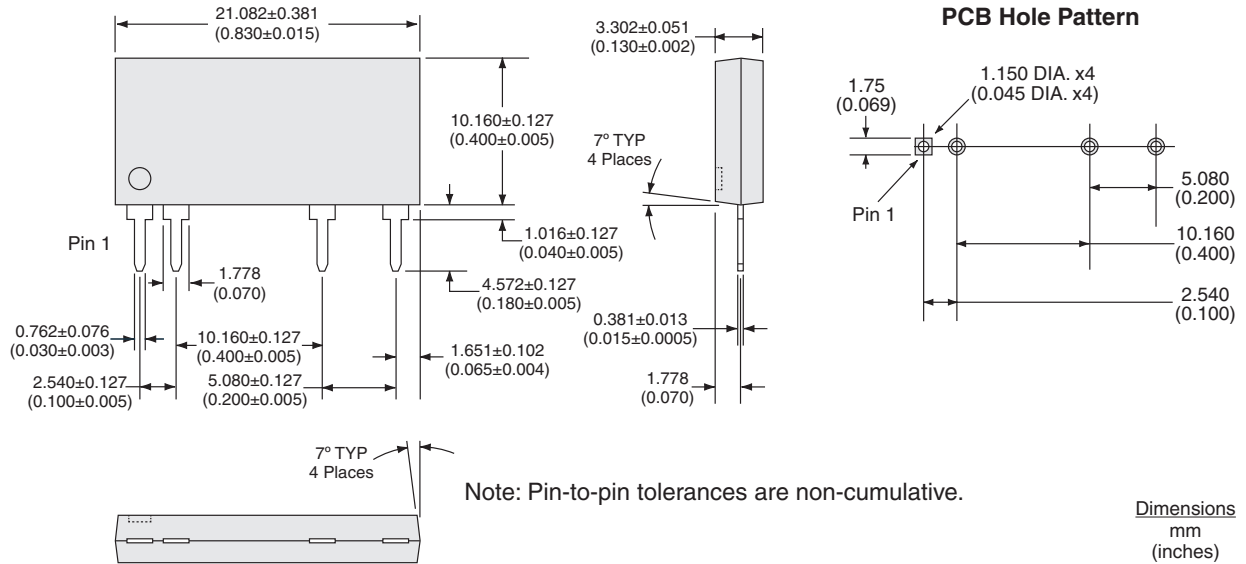
| Device | Maximum Solder Temperature | Maximum Body Temperature | Maximum Total Dwell Time | Wave Cycles |
|----------|----------------------------|--------------------------|--------------------------|-------------|
| CPC1706Y | 260°C | 245°C | 10 seconds | 1 |

Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.



MECHANICAL DIMENSIONS



For additional information please visit our website at: <https://www.ixysic.com>