TPSMB Asymmetric Series

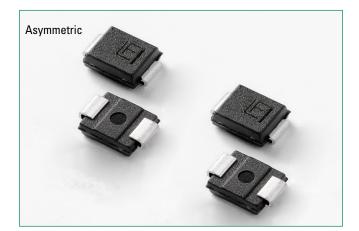
Surface Mount - 600W











Maximum Ratings and Thermal Characteristics

 $(T_{\Delta}=25^{\circ}C \text{ unless otherwise noted})$

Parameter	Symbol	Value	Unit
raidilietei	Зуппоп	value	Oiiit
Peak Pulse Power Dissipation	P _{PPM} ¹		
$(I_{PP} \times V_{C})$ by 10/1000µs waveform		600	W
(Fig.1)(Note 1), (Note 2)	P _{PPM} ²		
Power Dissipation on infinite heat	P _{M(AV)}	5.0	W
sink at T _L =50°C	· M(AV)	0.0	
Operating Junction Temperature	T,	-65 to 175	
Range	١,	03 to 173	°C
Storage Temperature Range	T _{STG}	-65 to 175	
Typical Thermal Resistance Junction		00	0000
to Lead	$R_{\theta JL}$	20	°C/W
Typical Thermal Resistance Junction	Р	100	00001
to Ambient	$R_{\theta JA}$	100	°C/W
Typical Junction Capacitance	C,	650	pF

Notes:

- 1. Non-repetitive current pulse, per Fig.4 and derated above $T_{\rm A}$ =25°C per Fig. 3.
- 2. Mounted on copper pad area of 0.2x0.2" (5.0 x 5.0mm) to each terminal

Description

The TPSMB Asymmetric Series is designed specifically to protect sensitive electronic equipment from voltage transients induced by lightning and other transient voltage events.

Features

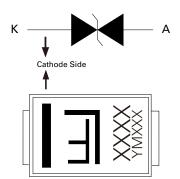
- High reliability application and automotive grade AEC-Q101 qualified
- Surface mount component to optimize board space
- Low profile package
- Typical failure mode is short from over-specified voltage or
- Whisker test is conducted based on JEDEC JESD201A per its table 4a and 4c
- ESD protection of data lines in accordance with IEC 61000-4-2, 30kV(Air), 30kV (Contact)
- EFT protection of data lines in accordance with IEC 61000-
- Glass passivated chip junction
- 600W P_{PPM} peak pulse power capability at 10/1000µs waveform, repetition rate (duty cycles):0.01%

- Fast response time: typically less than 1.0ns from 0V to V_{RR}
- Excellent clamping capability
- Low incremental surge resistance
- UL Recognized compound meeting flammability rating
- Meet MSL level1, per J-STD-020, High temperature soldering guaranteed: 260°C/10 seconds at terminals
- Matte tin lead-free plated
- Halogen free and RoHS compliant
- Pb-free E3 means 2nd level interconnect is Pb-free and the terminal finish material is tin(Sn) (IPC/JEDEC J-STD-609A.01)

Applications

TVS components are ideal for the protection of I/O Interfaces, VCC bus and other vulnerable circuits used in Automotive applications.

Pin out & Functional Diagram



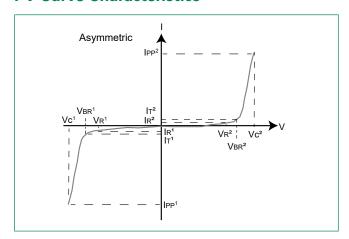


TPSMB Asymmetric SeriesSurface Mount – 600W

Electrical Characteristics (T_A=25°C unless otherwise noted)

		к ————————————————————————————————————						— A							
Part Number Markin	Marking	Maximum Stand Volume Stand In the Stand In t	Stand V	Volta	kdown age V _{BR} ts) @ L ₁ ¹ Clamping	Peak lest Pulse Curren Current I _T 1	Test Current	Reverse	Stand off Voltage V _R ² (Volts)	Breakdown Voltage V _{BR} (Volts) @ I _T ²		Clamping	Peak	Test Current	
			MIN	MAX	Voltage V _c ¹@ I _{pp} ((V)		I _T ¹ (mA)	Ι _R ² @ V _R ² (μΑ)		MIN	MAX	Voltage V _C ² @ I _{pp} (V)	. 2	I _T ² (mA)	
TPSMB2616CA	2616	1	26	28.9	31.9	42.1	14.3	1	1	16	17.8	19.7	26	23.1	1

I-V Curve Characteristics



- \mathbf{P}_{PPM} Peak Pulse Power Dissipation ($_{\text{IPP}} \times V_{\text{C}})\!\!-\!\!\text{Max}$ power dissipation
- $\mathbf{V_n} \mathbf{VV_n^2} \mathbf{Stand\text{-}off} \, \mathbf{Voltage} \mathbf{Maximum} \, \mathbf{voltage} \, \, \mathbf{that} \, \mathbf{can} \, \mathbf{be} \, \mathbf{applied} \, \mathbf{to} \, \mathbf{the} \, \mathbf{TVS} \, \mathbf{without} \, \mathbf{operation}$
- V_{gR}'/V_{gR}^2 Breakdown Voltage Maximum voltage that flows though the TVS at a specified test current (I_7)
- $V_{\rm c}/V_{\rm c}^2$ Clamping Voltage Peak voltage measured across the TVS at a specified lppm (peak impulse current)
- I_R^1/I_R^2 Reverse Leakage Current Current measured at V_R

Ratings and Characteristic Curves (T_A=25°C unless otherwise noted)

Figure 1:TVS Transients Clamping Waveform

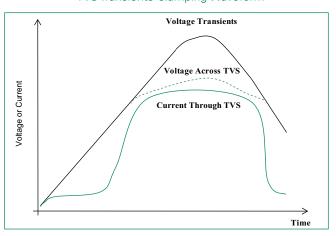
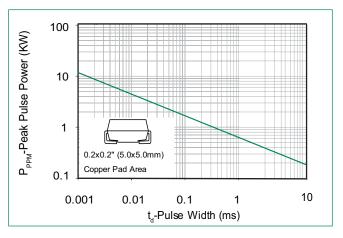


Figure 2:
Peak Pulse Power Rating Curve





TPSMB Asymmetric Series Surface Mount – 600W

Ratings and Characteristic Curves (T_A=25°C unless otherwise noted) (Continued)

Figure 3: Peak Pulse Power Derating Curve

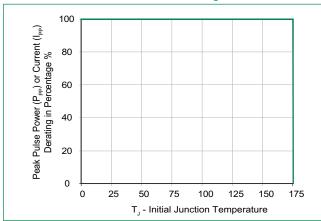
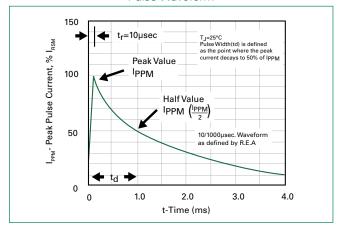
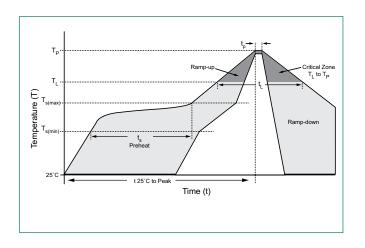


Figure 4: Pulse Waveform



Soldering Parameters

Reflow Co	ndition	Lead-free assembly	
	- Temperature Min (T _{s(min)})	150°C	
Pre Heat	- Temperature Max (T _{s(max)})	200°C	
	-Time (min to max) (t _s)	60 - 120 secs	
Average ra to peak	mp up rate (Liquidus Temp (T _L)	3°C/second max	
T _{S(max)} to T _L	- Ramp-up Rate	3°C/second max	
Reflow	- Temperature (T _L) (Liquidus)	217°C	
	-Time (min to max) (t _s)	60 - 150 seconds	
Peak Temp	erature (T _p)	260 ^{+0/-5} °C	
Time within Temperatu	in 5°C of actual peak re (t _p)	30 seconds max	
Ramp-dow	vn Rate	6°C/second max	
Time 25°C	to peak Temperature (T _p)	8 minutes max.	
Do not exc	eed	260°C	



Physical Specifications

Weight	0.003 ounce, 0.093 grams
Case	JEDEC DO214AA. Molded plastic body over glass passivated junction
Polarity	Color band denotes cathode for unidirectional components
Terminal	Matte Tin-plated leads, Solderable per JESD22-B102

Environmental Specifications

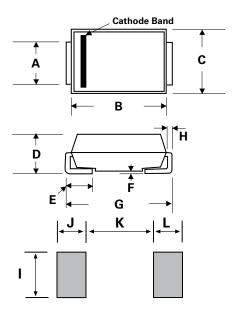
High Temp. Storage	JESD22-A103
HTRB	JESD22-A108
Temperature Cycling	JESD22-A104
MSL	JEDEC-J-STD-020, Level 1
H3TRB	JESD22-A101
RSH	JESD22-A111



TPSMB Asymmetric Series Surface Mount – 600W

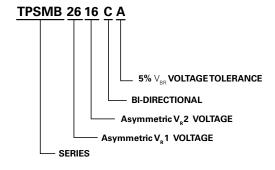
Dimensions

DO-214AA (SMB J-Bend)

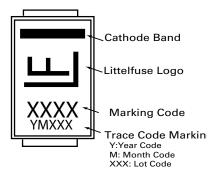


Dimensions	Incl	hes	Millimeters			
Dimensions	Min	Max	Min	Max		
Α	0.077	0.086	1.950	2.200		
В	0.160	0.180	4.060	4.570		
С	0.130	0.155	3.300	3.940		
D	0.084	0.096	2.130	2.440		
E	0.030	0.060	0.760	1.520		
F	-	0.008	-	0.203		
G	0.205	0.220	5.210	5.590		
Н	0.006	0.012	0.152	0.305		
1	0.089	-	2.260	-		
J	0.085	-	2.160	-		
K	-	0.107	-	2.740		
L	0.085	-	2.160	-		

Part Numbering System



Part Marking System

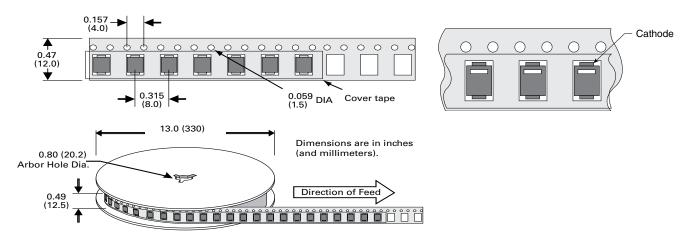


Product Selector & Packaging Option

Part number	Marking Code	Component Package	Quantity	Packaging Option	Packaging Specification	
TPSMB2616CA	2616	DO-214AA	3000	Tape & Reel - 12mm tape/13" reel	EIA STD RS-481	



Tape and Reel Specification



Disclaimer Notice - Information furnished is believed to be accurate and reliable. However, users should independently evaluate the suitability of and test each product selected for their own applications. Littleffuse products are not designed for, and may not be used in, all applications. Read complete Disclaimer Notice at http://www.littleffuse.com/disclaimer-electronics.

